

**SYLLABUS**



Due	#	Date	Topics
	1	M 12 Jan	Course Intro, procedures
	2	W 14	CIRCUITS: Basic DC Circuits: Units, Voltage v, Current i, Power p
	3	F 16	CIRCUITS: Basic DC Circuits: EM simp., Devices, Passive sign <b>Ex 1</b>
		M 19	<i>HOLIDAY: MARTIN LUTHER KING DAY</i>
	4	W 21	CIRCUITS: Basic DC Circuits: Sources: voltage, current, ind, dep <b>Ex 1</b>
HW 1	5	F 23	CIRCUITS: Kirchhoff's Laws: Wrtg v, i eqns: <b>Ex 1</b>
HW 2	6	M 26	CIRCUITS: Ohm's Law: R nets <b>Ex 1</b> .; V/I Dividers <b>Ex</b>
HW 3	7	W 28	HW 3 solution
	8	F 30	OP-AMPS: As high-gain differential amplifier, as ideal amplifier, <b>Ex 2</b>
HW 4	9	M 2 Feb	HW 4 solution
	10	W 4	<b>Exam 1</b>
	11	F 6	Exam 1 solution
	12	M 9	CIRCUITS: Basic DC Circuits: Power; Node-Voltage Method <b>Ex 1</b>
HW 5	13	W 11	CIRCUITS: Node-Voltage Method <b>Ex 4</b>
HW 6	14	F 13	CIRCUITS: Mesh-Current Method <b>Ex 1</b>
	15	M 16	<i>HOLIDAY: PRESIDENT'S DAY</i>
HW 7	16	W 18	HW 7 solution
	17	F 20	CIRCUITS: Thevenin Equivalent: Thevenin $\leftrightarrow$ Norton xform <b>Ex.</b> ; <b>Ex</b>
HW 8		M 23	HW 8 solution
	18	W 25	<b>Exam 2</b>
	19	F 27	Exam 2 solution
	20	M 2 Mar	RLC CIRCUITS: C (Capacitor Eqns): <b>Ex 1</b> ; L (Inductor Eqns): <b>Ex 1</b>
	21	W 4	RLC CIRCUITS: General RC/RL Solution: <b>Ex 1</b>
	22	F 6	RLC CIRCUITS: General RC/RL Solution: <b>Ex 3</b>
HW 9	23	M 9	RLC CIRCUITS: General RC/RL Solution: <b>Ex 6</b>
HW 10	24	W 11	CIRCUITS: Max Pwr Xfer: <b>Ex</b>
HW 11	25	F 13	HW 11 solution
	26	M 16	<i>HOLIDAY: SPRING BREAK</i>
	27	W 18	<i>HOLIDAY: SPRING BREAK</i>
	28	F 20	<i>HOLIDAY: SPRING BREAK</i>
		M 23	SUPERPOSITION: Circuits: VDC+VDC: <b>Ex 1</b>
HW 12		W 25	HW 12 solution
		F 27	<b>Exam 3</b>
	29	M 30	Exam 3 solution
	30	W 1 Apr	COMPLEX ANALYSIS: Basic Math: 10 views $j=\sqrt{-1}$ , rationalization <b>Ex 2</b>
	31	F 3	COMPLEX ANALYSIS: Basic Math: 10 views $j=\sqrt{-1}$ , Add sub <b>Ex</b>
	32	M 6	COMPLEX ANALYSIS: <b>Phasors</b> ; IMPEDANCE: Ohm's Law: <b>Ex 1, Ex 2</b>
HW 13	33	W 8	IMPEDANCE CIRCUITS: Kirchhoff's Laws: <b>Ex</b>
	34	F 10	IMPEDANCE CIRCUITS: Node-Voltage Method: <b>Ex</b>
HW 14	35	M 13	IMPEDANCE CIRCUITS: Thevenin Equivalent: <b>Ex 1</b>
HW 15	36	W 15	HW 15 solution
	37	F 17	SUPERPOSITION: Circuits: VAC+VAC: <b>Ex</b>
HW 16	38	M 20	HW 16 solution
	39	W 22	<b>Exam 4</b>
	40	F 24	Exam 4 solution
HW 17	41	M 27	HW 17 solution
HW 18	42	W 29	HW 18 solution
		H 7 May	<b>Final Exam</b> (8:00 AM – 10:00 AM, regular classroom)