

| Due | # | Date | Topics |
|-------|---------|---|---|
| | 1 | M 10 Jan | Course Intro, procedures |
| | 2 | W 12 | CIRCUITS: Basic DC Circuits: Units, Voltage v, Current i, Power p |
| | 3 | F 14 | CIRCUITS: Basic DC Circuits: EM simp., Devices, Passive sign Ex 1 |
| | | M 17 | HOLIDAY: MARTIN LUTHER KING DAY |
| | 4 | W 19 | CIRCUITS: Basic DC Circuits: Sources: voltage, current, ind, dep Ex 1 |
| HW 1 | 5 | F 21 | CIRCUITS: Kirchhoff's Laws: Wrtg v, i eqns: Ex 1 |
| HW 2 | 6 | M 24 | CIRCUITS: Ohm's Law: R nets Ex 1 ; V/IDividers Ex |
| HW 3 | 7 | W 26 | HW 3 solution |
| | 8 | F 28 | OP-AMPS: As high-gain differential amplifier, as ideal amplifier, Ex 2 |
| HW 4 | 9 | M 31 | HW 4 solution |
| | 10 | W 2 Feb | Exam 1 |
| | 11 | F 4 | Exam 1 solution |
| | 12 | M 7 | CIRCUITS: Basic DC Circuits: Power; Node-Voltage Method Ex 1 |
| HW 5 | 13 | W 9 | CIRCUITS: Node-Voltage Method Ex 4 |
| HW 6 | 14 | F 11 | CIRCUITS: Mesh-Current Method Ex 1 |
| HW 7 | 15 | M 14 | HW 7 solution |
| | 16 | W 16 | CIRCUITS: Thevenin Equivalent: Thevenin<->Norton xform Ex ,; Ex |
| HW 8 | 17 | F 18 | HW 8 solution |
| | | M 21 | HOLIDAY: PRESIDENT'S DAY |
| | 18 | W 23 | Exam 2 |
| | 19 | F 25 | Exam 2 solution |
| | 20 | M 28 | RLC CIRCUITS: C (Capacitor Eqns): Ex 1 ; L (Inductor Eqns): Ex 1 |
| | 21 | W 2 Mar | RLC CIRCUITS: General RC/RL Solution: Ex 1 |
| | 22 | F 4 | RLC CIRCUITS: General RC/RL Solution: Ex 3 |
| HW 9 | 23 | M 7 | RLC CIRCUITS: General RC/RL Solution: Ex 6 |
| HW 10 | 24 | W 9 | CIRCUITS: Max Pwr Xfer: Ex |
| HW 11 | 25 | F 11 | HW 11 solution |
| | 26 | M 14 | SUPERPOSITION: Circuits: VDC+VDC: Ex 1 |
| HW 12 | 27 | W 16 | HW 12 solution |
| | 28 | F 18 | Exam 3 |
| | | M 21 | HOLIDAY: SPRING BREAK |
| | | W 23 | HOLIDAY: SPRING BREAK |
| | | F 25 | HOLIDAY: SPRING BREAK |
| | 29 | M 28 | Exam 3 solution |
| | 30 | W 30 | COMPLEX ANALYSIS: Basic Math: 10 views $j=\sqrt{-1}$, rationalization Ex 2 |
| | 31 | F 1 Apr | COMPLEX ANALYSIS: Basic Math: 10 views $j=\sqrt{-1}$, Add sub Ex |
| | 32 | M 4 | COMPLEX ANALYSIS: Phasors ; IMPEDANCE: Ohm's Law: Ex 1 , Ex 2 |
| HW 13 | 33 | W 6 | IMPEDANCE CIRCUITS: Kirchhoff's Laws: Ex |
| | 34 | F 8 | IMPEDANCE CIRCUITS: Node-Voltage Method: Ex |
| HW 14 | 35 | M 11 | IMPEDANCE CIRCUITS: Thevenin Equivalent: Ex 1 |
| HW 15 | 36 | W 13 | HW 15 solution |
| | 37 | F 15 | SUPERPOSITION: Circuits: VAC+VAC: Ex |
| HW 16 | 38 | M 18 | HW 16 solution |
| | 39 | W 20 | Exam 4 |
| | 40 | F 22 | Exam 4 solution |
| HW 17 | 41 | M 25 | HW 17 solution |
| HW 18 | 42 | W 27 | HW 18 solution |
| | M 2 May | Final Exam (8:00 AM – 10:00 AM, regular classroom) | |