

- a) The above circuit operates in linear mode. Derive a symbolic expression for v_0 . The expression must contain not more than the parameters i_{s1} , i_{s2} , R_1 , R_2 , and R_3 .
- b) If $i_{s1} = 0 \mu A$, find the value of R_3 that will yield an output voltage of $v_0 = 1 \text{ V}$ when $i_{s2} = 10 \mu A$.
- c) Derive a symbolic expression for v_0 in terms of common mode and differential input currents:

$$i_{\Sigma} = \frac{i_{s1} + i_{s2}}{2}$$
 and $i_{\Lambda} = \frac{i_{s1} - i_{s2}}{2}$

The expression must contain not more than the parameters i_{Σ} , i_{Δ} , R_1 , R_2 , and R_3 . Write the expression as i_{Σ} times a term plus i_{Δ} times a term. Hint: start by writing i_{s1} and i_{s2} in terms of i_{Σ} and i_{Δ} :

$$i_{s1} \equiv i_{\Sigma} + i_{\Delta}$$
 and $i_{s2} \equiv i_{\Sigma} - i_{\Delta}$

$$R_{jn} = \frac{V_{X}}{I_{S2}} d$$

If $i_{\Delta} = 0$ and $R_1 = R_2$, write a formula for the current flowing from left to right in R_3 as a function of not more (and possibly less) than the following terms: i_{Σ} , R_1 , R_2 , and R_3 .

solfn: a) First, we find
$$v_p$$
 (voltage at + input):
 $v_p = i_{BZ} R_Z$
Second, we find the durrent flowing
toward the - input from the left,
using v_n (voltage at - input) = v_p :
 $i_p = i_{S1} - \frac{v_n}{R_1} = i_{S1} - \frac{i_{SZ}R_Z}{R_1}$
Third, we find the current flowing
in the feedback resistor, R_3 , from
left to right:
 $i_r = \frac{v_n - v_o}{R_3} = \frac{i_{SZ}R_Z - v_o}{R_3}$
Fourth, we set $i_r = i_f$ and solve for v_o :
 $i_{S1} - \frac{i_{SZ}R_Z}{R_1} = \frac{i_{SZ}R_Z - v_o}{R_3}$
or $v_o = -i_{S1}R_3 + i_{BZ}R_Z \left(1 + \frac{R_3}{R_1}\right)$
b) $1V = IOMA \cdot 2k_B \left(1 + \frac{R_3}{2k_B}\right) = 20mV \left(1 + \frac{R_3}{2k_B}\right)$
 $\therefore 1 + \frac{R_3}{2k_B} = 50$ or $\frac{R_3}{2k_B} = 49$
or $R_3 = 90 k_SZ$

c)
$$v_{0} = -(i_{2}+i_{d})R_{3} + (i_{2}-i_{d})R_{2} \left(1+\frac{R_{3}}{R_{1}}\right)$$

or $v_{0} = i_{2} \left(R_{2}+R_{2}R_{3}-R_{3}\right)$
 $- i_{d} \left(R_{2}+R_{2}R_{3}+R_{3}\right)$
d) For $i_{d}=0$ and $R_{1}=R_{2}$, we have
 $v_{0} = i_{2} \left(R_{2}+\frac{p_{3}}{R_{3}}-\frac{p_{3}}{R_{3}}\right)$
or $v_{0} = i_{2} R_{2}$
Then $i_{R_{3}} = \frac{V_{n}-V_{0}}{R_{3}} = \frac{V_{n}-\hat{i}_{2}R_{2}}{R_{3}}$
But $v_{n} = v_{p} = i_{\beta 2}R_{2} = i_{\beta 1}+i_{\beta 2}R_{2}$
(since $\hat{i}_{\beta 1}=\hat{i}_{\beta 2}$) $= i_{\beta 2}R_{2}$
Then $i_{R_{3}} = \frac{\hat{i}_{2}R_{2}-\hat{i}_{2}R_{2}}{R_{3}} = 0A$
Note: When $\hat{i}_{\beta 1}=\hat{i}_{\beta 2}$, the current in
 R_{1} and R_{2} is the same (since $v_{n}=v_{p})$,
so there is no current (eft over
to flow in R_{3}.

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or
$$\frac{2V_1}{3\Omega} = 15A$$

or $V_1 = \frac{3}{2} \cdot 15A = 22.5 V$

To find V_{Th}, we again use avoltage-divider formula:

$$V_{Th} = V_1 \cdot \frac{2\Omega}{1 + 2\Omega} = 22.5 V \cdot \frac{2}{3} = 15 V$$

Note: Another approach is to replace the dependent source with a resistor. To do so, we write the voltage across the dependent source in terms of dependent variable V_X . From an egh above, we have $V_1 = 3V_X$.

$$V_X \downarrow V_1 = 3V_X$$

$$R_{eg} = \frac{V}{L} = \frac{3V_{x}}{V_{x}} = 3\Omega$$

$$V_{1} \quad I\Omega$$

$$R_{eg} = \frac{15A}{15A} + 2\Omega$$

$$V_{1} = 15A \cdot 3\Omega || 3\Omega = 22.5V$$

Note: Reg changes with R across a, b.

One way to find RTh is to use $R_{Th} = \frac{v_{Th}}{\hat{i}_{sd}}$ where ise = short circuit from a to b $v_{1}=v_{x}$ in ov q v_{x} $v_{$ We may ignore the 252 resistor that is shorted out. Node v_1 egin: (Note that $v_x = v_1$.) $v_1 - 15A + \frac{v_1}{10} = 0A$ or $\frac{2V_1}{12} = 15A$ or $V_1 = 15A \cdot 1\Omega = 7.5V$ Our current is $\hat{l}_{SC} = \underline{V}_1 = 7.5V = 7.5A$. $R_{Th} = \frac{V_{Th}}{L_{sc}} = \frac{15V}{7.5A} = 2 \Omega$ Note: we could replace the dependent source with $\text{Reg} = \frac{V}{i} = \frac{Vx}{Vx} = 1$ so for isc.







After being open for a long time, the switch closes at time $t = t_0$.



- a) Choose either an *R* or *L* to go in box **a** and either an *R* or *L* to go in box **b** to produce the $v_0(t)$ shown above. (You will need one *R* and one *L*. Use an *R* value of 1.3 k Ω . Also, note that v_0 stays low forever after $t_0 + 16 \mu s$.) Specify which element goes in each box and its value.
- b) Sketch $v_1(t)$, showing numerical values appropriately.
- c) Sketch $v_2(t)$, showing numerical values appropriately.
- d) Sketch $v_3(t)$. Show numerical values for $t < t_0$, for $t_0 < t < t_0 + 16 \,\mu\text{s}$, and for $t > t_0 + 16 \,\mu\text{s}$. Use the ideal model of the diode: when forward biased, its resistance is zero; when reverse biased, its resistance is infinite.

soln: a) We first find V, which is constant. The IKA resistor and 2mA source are adross the GV source. Because they are across the GV source, they may be ignored. $V_1 = 6V$ regardless of what is across the 6V source,

 $V_1 = 6 V$

For boxes a and b, we observe that, if we had an L in b, that L will act like a wire at t=0. This would make $v_2(0^-) = 0V$. The op-amp acts like a comparator, with the output equal to ±10V according to the sign of $V_2 - V_1$. For $V_2 = 0V$ and $V_1 = 6V$ we would have $v_2 - v_1 = 0 - 6V = -6V$ and $v_o = -10V$ at $t = 0^{-1}$. This differs from the plot of Vo(t) given in the problem statement. Thus, b must contain an R rather than an L.

Because the output voltage, Vo(t), changes 16 us after the switch moves, box a must contain an L to give the circuit a time-varying behavior.

> a = Lb = R = 1.3 k (from prob statement)

At $t=0^{-}$, we have the following equivalent circuit:



$$\frac{1}{\sqrt{2}} \frac{1}{\sqrt{2}} \frac{1}{\sqrt{2}$$





$$j_{2k,2k} \underbrace{z_{k,2}}_{(k+j)} \underbrace{(+j)}_{(k+j)} \underbrace{(+j)}_{(k+j)} \underbrace{-j_{2k,2}}_{V_{1}} \underbrace{+}_{V_{1}}_{V_{1}}$$
Now we observe that the $j_{2k,2}$ and $-j_{2k,2}$ in parallel are equivalent to an open circuit:

$$j_{2k,2} \underbrace{|-j_{2k,2} = j_{2k,2} \cdot ||_{-1} = j_{2k,2} \underbrace{+}_{0} = \infty_{2}}_{O}$$
Thus, the L and C disappear:

$$z_{k,2} \underbrace{(+j)}_{O} \underbrace{+}_{V_{1}}_{-}$$
Now we use Ohm's Law:

$$V_{1} = (1+j) \operatorname{MA} \cdot 2k_{2} = \underbrace{2+j_{2}}_{V} \underbrace{+}_{0} \underbrace{+}_{V_{1}}_{V_{2}}$$
b) In polar form, $V_{1} = 2\sqrt{2}^{2} \angle + 45^{\circ} V$
In the time domain, we have

$$V_{1}(t) = 2\sqrt{2}^{2} \cos(500t + 45^{\circ}) V$$
Note: We could also directly take the inverse phasor of $2+j_{2}V$:

$$V_{1}(t) = 2\cos(500t) - 2\sin(500t) V$$



$$V_n = i_3 \cdot R_1 R_2$$

Third, we find the value of V_0 that yields the above value of v_n .

Since no current flows into the op-amp inputs, no current flows in R3, and R3 has no voltage drop.

$$v_0 = v_n - V_s$$

or $v_0 = i_s \cdot R_1 \| R_2 - V_s$

b) Given $V_S = OV$ and $i_S = 1 \text{ mA}$ we are to find the value of R_2 that yields $V_0 = 1V$.

> Using the expression in (a) for V_0 we have $IV = I M A \cdot 2k \Omega || R_2 - OV$

2k2 R2 =1k2

or

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or

$$R_2 = 2 k \Omega$$

c) We have IV = OA . 2KIE ZK2 - VS

d) From part (a), we have the following: $V_1 = i_5 \cdot R_1 || R_2$ $R_{in} \equiv \frac{V_1}{i_5} = R_1 || R_2 = 1 k \Omega$ EEE 1270 Su 08

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Ex:



- a) Find the Thevenin equivalent of the above circuit relative to terminals **a** and **b**.
- b) If we attach R_L to terminals **a** and **b**, find the value of R_L that will absorb maximum power.
- c) Calculate the value of that maximum power absorbed by $R_{\rm L}$.

solin: a)
$$V_{Th} = V_{a,b}$$
 no load

With nothing connected from **a** to **b**, $i_X = 0$ and $50i_X = 0V$ acts like a wire



Since ix=0, there is no V-drop across the 5 k.Q resistor. An outer voltage loop reveals that v_{th} = 10V. Because we have a dependent source, we can find RTh using the formula



Although we have a dependent source, the IOV source between the top and bottom rails makes the SOix and 3k.2. components irrelevant.

From an outer V-loop, we have

$$\therefore R_{\text{Th}} = \frac{V_{\text{Th}}}{\hat{\iota}_{sd}} = \frac{10V}{2mA} = 5 \text{ k.s.}$$

Note: The IOV source across the rails allows to ignore the 50 ix and 3k.g. We may remove them. Then we observe that we are left with the Thevenin equivalent direct: VTh = IOV, RTH = 5k.f.



Pmax = 5 mW



After being in position **c** for a long time, the switch moves from **c** to **d** at $t = t_0$.

Rail voltages = ± 12 V



- a) Choose either an *R* or *C* to go in box **a** and either an *R* or *C* to go in box **b** to produce the $v_0(t)$ shown above. (Note that v_0 stays high forever after $t_0 + 2$ ms.) Specify which element goes in each box and its value.
- b) Sketch $v_1(t)$, showing numerical values appropriately.
- c) Sketch $v_2(t)$, showing numerical values appropriately.
- d) Sketch $v_3(t)$. Show numerical values for $t < t_0$, for $t_0 < t < t_0 + 2$ ms, and for $t_0 + 2$ ms < t. Use the ideal model of the diode: when forward biased, its resistance is zero; when reverse biased, its resistance is infinite.

soln: a) For vo to be low, (i.e., -12V), we must have
$$v_2 < v_1$$
.
To find v_1 , we slide the 4V source through the 6KD resistor and find that we have the equivalent of a -15V source and a voltage divider formed by the 3KD and 6KD resistors.
 $v_1 = -15V \cdot 3KD = -5V$
 $R = R$
 $k = C$.
At $k = 0^{-}$, we must have $v_2 < -5V$.
This is possible only if box a contains a resistor and box b contains a capacitor. If a is an R and b is a C, then the C will charge wrill $v_2 = -10V < v_1$.
When the switch mores from c to d, the capacitor voltage start charging toward OV, but it will still be $-10V$ initially. This gives the desired waveform for $v_2(t)$: v_0 will go high when $v_2 = v_1 = -5V$.
Note: The reasons why other companents in boxes A and b fail to yield the desired $v_0(t)$ are as follows:

a = R and **b** = R cannot give a waveform that changes after a delay. *Vo* would have to change instantly at t= to.

a = C and **b** = R would result in C charging until no current flows in R. This means $v_2 = 0V$, or $v_2 > v_1$, causing v_0 to be high before $t = t_0$.

a = C and b = C would result in an arbitrary voltage at v2. The total voltage drop across the two C's would be 101. When the switch changes from $c \leftarrow d$, the capacitors would charge until the total voltage drop across them was OV. The same current would flow in both C's, causing a voltage change that would be inversely proportional to the C values. The waveform shown for Volt) could be produced, but there is a lack of control over the initial value of V2. This would make the timing of the volt) waveform uncertain. Thus, we reject this solution.

Now we find possible values for R and C. We have the following circuit model for t>t; Siks $v_{\mathcal{C}}(t > t_{0}) = v_{\mathcal{C}}(t \Rightarrow \infty) + \left[v_{\mathcal{C}}(t_{0}^{+}) - v_{\mathcal{C}}(t \Rightarrow \infty)\right] =$ $\sim 10V$ øν $-t/\tau$ $V_{c}(t>t_{o})=-10e$ V (where we take $t_{o}=0$) where $\tau = (R+1kz)c$ We want $v_c(\pm \pm 2ms) = v_1 = -5V$ $-10e^{-2ms/t}$ or final $e^{-2wd/t}$ -2 ms = 7 In 1/2 $\mathcal{T} = \frac{ZMS}{.enz} \doteq 2.9 \text{ ms}$ One solution R = 1.9 kg and C = 1.4 F. Note: R=OSZ is min R, C= 2.94 Fis max C.

сv







