

A transistor has three terminals-- the base, the collector, and the emitter. The current flow from the collector to the emitter (through the transistor) is controlled by the current flow from the base to the emitter. A small base current can control a much larger collector current. Often they are related by a simple factor, called beta ( $\beta$ ). For a given base current, the transistor will allow  $\beta$  times as much collector current. The key word here is *allow*. The transistor doesn't make the current flow-- some outside power source does that. It simply regulates the current like the valve above. Big power transistors usually have a  $\beta$ s between 20 and 100. For little signal transistors,  $\beta$  is usually between 100 and 400. Darlington transistors (really two transistors in one package) can have  $\beta$ s in the 1000s.

A transistor can be used as a current controlled switch. When there's no base current, it's off, like an open switch. When there is a base current, it's on. If something outside of the transistor is limiting the collector current to less than  $\beta$  times the base current then the transistor will turn on as much as it can, like a closed switch. A transistor that is off is operating in its "cutoff" region. A transistor that is fully on is operating in its "saturation" region. A transistor that is partially on is in active control of its collector current ( $\beta$  times the base current) and is operating in its "active" region. (Note the valve analogy has a problem with the "open" and "closed" terms.)

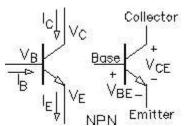
There are many types of transistors. PNP transistors work like the NPN transistors, except that all the currents and voltages are backwards. Field-effect transistors (FETs) are are controlled by voltage instead of current and come in many varieties. In this class we'll only work with NPN transistors.

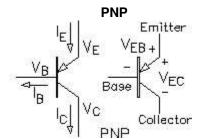
Silicon diodes are made of two layers of doped silicon, a P layer is the anode and an N layer is the cathode. A P-N junction is a diode. Anode

Bipolar junction transistors (BJTs) consist of three layers of doped silicon. The NPN transistor has a thin layer of P-doped silicon sandwiched between two layers of N-doped silicon. Each P-N junction can act like a diode. In fact, this is a fairly good way to check a transistor with an ohmmeter (set to the diode setting).

The base-emitter junction always acts like a diode, but because the base is very thin, it makes the other junction act like a controlled valve (you probably don' t want to know the details, so call it magic).

# **Transistor Symbols**





Replace  $v_{BF}$  with  $v_{FB}$  and

 $v_{CF}$  with  $v_{FC}$  in equations below

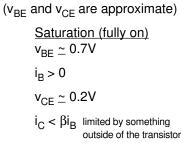
Notice the subscripts

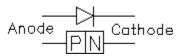
 $v_{BE} = v_B - v_E$ 

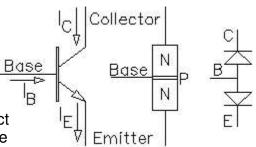
 $v_{CE} = v_{C} - v_{E}$ 

## Modes or regions of operation

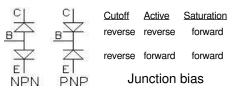
<u>Cutoff (off)</u> v <sub>BE</sub> < 0.7V	$\frac{Active (partially on)}{v_{BE} \simeq 0.7V}$
i <sub>B</sub> = 0	i <sub>B</sub> > 0
	$v_{CE} \ge 0.2V$
i <sub>C</sub> = 0	$i_{C} = \beta i_{B} = \alpha i_{E}$ $\alpha \simeq 1$ controlled by the transistor

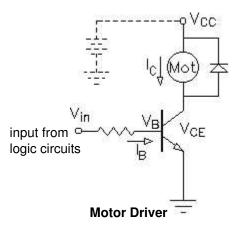






A bipolar junction transistor contains two diode junctions





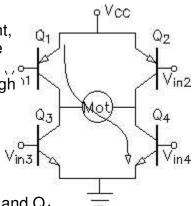
# The Transistor as a switch

One of the most common uses of a transistor is as a current-controlled switch. Transistor switches are the basis for all digital circuits, but that's probably not where you' II use the transistor. More likely, you' II want to control a high-current device, like a motor, with arintegrated-circuit output from a computer or logic circuit. The small integrated circuit won' t be able to supply enough current to run the motor, so you' II use a transistor to switch the larger current that flows through the motor. The input is hooked to the base of the transistor. (Often through a current limiting resistor, since  $V_B$  will only be 0.7V when the transistor is on.) A small  $I_B$  can switch on the much larger  $I_C$  and  $V_{CE}$  can be as low as 0.2V.

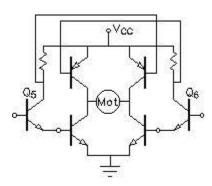
 $V_{CC}$ : The terminal marked  $V_{CC}$  above is just a circuit terminal hooked to a power supply, drawn in dotted lines here, but usually not shown at all. Power supply wires, like ground wires are often not shown explicitly on schematics. It makes the schematics a little less cluttered and easier to read.

**Diode:** If you' re switching an inductive load, like a motor, you should add a diode so that you' re not trying to switch off the motor current instantly. The diode (called a *flyback* diode when used like this) provides a path for the current still flowing through the motor when the transistor is switched off.

**H-bridge:** Of course, if you want to make the motor turn in both directions you' II need a more complex circuit. Look at the circuit at right, it's has the shape of an H, hence the name. If transistors G and  $Q_4$  are on, then the current flows as shown, left-to-right through the motor. If  $Q_4$  are transistors  $Q_2$  and  $Q_3$  are on, then the current flows the other way through 11 the motor and the motor will turn in the opposite direction. (The motor here is a permanent-magnet DC motor.) In my circuit, the top two transistors are PNPs, which makes the circuit more efficient. The H-bridge could also be made with all NPNs or with power MOSFET  $V_{in.3}^{O-1}$  transistors.



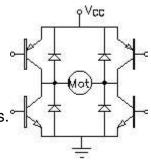
An H-bridge requires four inputs, all operated in concert. To turn on  $Q_1$  and  $Q_4$ , as shown,  $V_{in1}$  would have to be low and  $V_{in4}$  would have to be high. At the same time, the other two transistors would have to be off, so  $V_{in2}$  would have to be high and  $V_{in3}$  would have to be low.



If the control circuit makes a mistake and turns on  $Q_1$  and  $Q_3$  (or  $Q_2$  and  $Q_4$ ) at the same time you' II have a toaster instead of a motor driver, at least for a short while.

The circuit at left requires only two inputs. Transistors  $Q_5$  and  $Q_6$  work as *inverters*, when their inputs are high, their outputs are low and vice-versa. The resistors are known as *pull-up* resistors.

The H-bridge should also include flyback diodes.



# **Linear Amplifiers**

The objective of a linear amplifier is to output a faithful reproduction of an input signal, only bigger. A voltage amplifier makes the signal voltage bigger. A current amplifier makes the signal current bigger. Many amplifiers do both. All amplifiers should make the signal power bigger (depends somewhat on the load). Of course that means that they need a source of power, generally DC power from a battery or power supply. The signals are usually AC.

Unlike transistor switches, which operate in cutoff and saturation, linear amplifiers must operate in the active region. **Important relations:** (active region)

$$v_{BE} = v_B - v_E = 0.7 \cdot V$$
  $v_{CE} = v_C - v_E > 0.7 \cdot V$  ( $\simeq 0.2V$  if saturated)  
 $i_C = \beta \cdot i_B$   $i_C = \alpha \cdot i_E \simeq i_E$ 

# Bias:

Outside of the active region the input (base current) doesn't linearly control the output (collector current). To work as an linear amplifier, a transistor must operate in the active region. That means that the transistor must be turned on part way even when there's no signal at al Look back at the valve analogy, if small fluctuations in the horizontal pipe flow ( $i_B$ ) should produce larger but similar fluctuations in the vertical pipe flow ( $i_C$ ), then there must always be *some* flow. If either flow ever stops, the horizontal pipe flow ( $i_B$ ) is no longer in control.

To work in the active region  $i_B$  and  $i_C$  must be positive for all values of the AC signals.  $i_B$  and  $i_C$  must be *biased* to some positive DC value. We use capital letters ( $I_B$  and  $I_C$ ) for these DC bias values and lower case letters ( $i_b$  and  $i_c$ ) for the AC signals that will appear as fluctuations of these DC values

# Transistor Notes (BJT) p3

All voltages and currents can be shown in three different ways

e <sub>CAP</sub>			<u>meaning</u> DC, Bias
sm <sub>sm</sub>	<sup>v</sup> b	<sup>i</sup> c	AC, signal
sm <sub>CAP</sub>	<sup>v</sup> B	<sup>i</sup> C	DC and AC Together

The objective of bias then, is to partially turn on the transistor, to turn it, sort-of, half-way on. Now if I twiddle i<sub>B</sub>, i<sub>C</sub> will show a similar, but bigger, twiddle-- that' s the whole idea. The transistor should never go into cutoff for any expected input signal, otherwise you' II ge*clipping* at the output. Clipping is a form of distortion, where the output no longer looks like the input.

Furthermore, the transistor must not saturate. That will also cause clipping at the output.

Because  $\beta$  can vary widely from transistor to transistor of the same part number and V<sub>BF</sub> changes with temperature, achieving a stable bias can be a bit of a problem. Usually an emitter resistor  $(R_{F})$  is needed to stabilize the bias.

## DC Analysis in the active region

DC analysis applies to both switching and bias, although the circuits we' II look at here will include an  $R_F$  and we' II be working in the active region, meaning they are bias circuits. The key to DC analysis with an  $R_F$  is usually finding  $V_B$ .

The circuit at right shows a typical bias arrangement. The equations below are for that circuit, adapt them as necessary to fit your actual circuit.

## If you can neglect I<sub>B</sub>:

Often in quick-and-dirty analysis you can neglect the base current,  ${\rm I}_{\rm B}$ . In that case:  $\frac{P}{R_{B2}} \qquad V_E = V_B - 0.7 \cdot V \qquad I_E = \frac{V_E}{R_E} \simeq I_C \qquad V_C = V_{CC} - I_C \cdot R_C$ 

$$V_B = V_{CC} \cdot \frac{R_{B2}}{R_{B1} + R_B}$$

This assumption is OK if:  $R_{B1} \parallel R_{B2} \ll \beta R_E$ 

Quick check: R  $_{B1}$  < 10·R  $_E$  and/or R  $_{B2}$  < 10·R  $_E$  Should result in <10% error if  $\beta$  =100

# If you can't neglect I<sub>B</sub>:

Then you need to make a Thevenin equivalent of the base bias resistors.

$$V_{BB} = V_{CC} \frac{R_{B2}}{R_{B1} + R_{B2}}$$
  $R_{BB} = \frac{1}{\frac{1}{\frac{1}{R_{B1}} + \frac{1}{R_{B2}}}}$ 

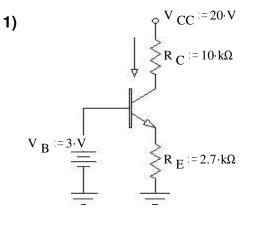
From the base' s point-of-view, the emitter resistor will look $\beta$  + 1) times bigger than it really is. This is because  $(\beta + 1)$  times as much current flows through R<sub>F</sub> than into the base. We can ignore the fact that the current is bigger if we pretend that the resistor is bigger. That leads to the simplified circuit. (Usually we use  $\beta$  as the factor rather than ( $\beta$  + 1), after all  $\beta$  just isn' t that well known anyway.)

$$I_{B} = \frac{V_{BB} - 0.7 \cdot V}{R_{BB} + \beta \cdot R_{E}} \qquad I_{C} = \beta \cdot I_{B} \simeq I_{E} \qquad V_{E} = I_{E} \cdot R_{E} \simeq I_{C} \cdot R_{E} \qquad V_{B} = V_{E} + 0.7 \cdot V$$
$$V_{C} = V_{CC} - I_{C} \cdot R_{C}$$
$$OR: \quad V_{B} = I_{B} \cdot \beta \cdot R_{E} + 0.7 \cdot V \qquad V_{E} = V_{B} - 0.7 \cdot V \qquad I_{E} = \frac{V_{E}}{R_{E}} \simeq I_{C} \qquad V_{C} = V_{CC} - I_{C} \cdot R_{C}$$

(Thevenin Eq.)

RB2 E

# Examples, DC (Bias) Analysis



 $\begin{array}{c|c} V_{CC} \coloneqq 20 \cdot V & \text{Given:} \\ V_B \coloneqq 3 \cdot V, \text{ regardless of current into base} \\ V_CC \coloneqq 20 \cdot V_R_C \coloneqq 10 \cdot k\Omega & R_E \coloneqq 2.7 \cdot k\Omega \\ \hline V_{CC} \coloneqq 20 \cdot V_R_C \coloneqq 10 \cdot k\Omega & R_E \coloneqq 2.7 \cdot k\Omega \\ \hline Find I_C, V_C, V_{CE}, \text{ and } P_Q \colon \\ \hline Solution: \\ V_E \coloneqq V_B - 0.7 \cdot V & V_E = 2.3 \cdot V \\ I_E \coloneqq \frac{V_E}{R_E} & I_E = 0.852 \cdot \text{mA} \simeq I_C \coloneqq I_E \\ V_C \coloneqq V_{CC} - I_C \cdot R_C & V_C = 11.48 \cdot V \\ V_{CE} \coloneqq V_C - V_E & V_{CE} = 9.18 \cdot V > 0.2V, \text{ OK, is in active region} \\ P_Q \coloneqq V_{CE} \cdot I_C & P_Q = 7.82 \cdot \text{mW} \\ \hline I_C \mid V_R C & \text{Given: may neglect } I_B \\ V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 8 \cdot k\Omega & R_{B2} \coloneqq 2 \cdot k\Omega & R_E \coloneqq 220 \cdot \Omega \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 8 \cdot k\Omega & R_{B2} \coloneqq 2 \cdot k\Omega & R_E \coloneqq 220 \cdot \Omega \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash$ 

2)  

$$V_{CC} = 10 \cdot V$$

$$R_{B1} = 8 \cdot k\Omega$$

$$V_{C} = 7.0 \cdot V$$

$$R_{B2} = 2 \cdot k\Omega$$

$$R_{B2} = 2 \cdot k\Omega$$

$$R_{C} := \frac{V_{CC} - V_{C}}{I_{C}} \qquad R_{C} = 508 \cdot \Omega$$
$$I_{RB2} := \frac{V_{B}}{R_{B2}} \qquad I_{RB2} = 1 \cdot mA$$

Given: may neglect I<sub>B</sub>  

$$V_{CC} := 10 \cdot V \quad V_C := 7.0 \cdot V \quad R_{B1} := 8 \cdot k\Omega \quad R_{B2} := 2 \cdot k\Omega \quad R_E := 220$$
  
Find V<sub>B</sub>, V<sub>E</sub>, I<sub>C</sub>, R<sub>C</sub>, V<sub>CE</sub>, I<sub>RB2</sub>, and P<sub>G</sub>:  
Solution:  
 $V_B := V_{CC} \cdot \frac{R_{B2}}{R_{B1} + R_{B2}} \quad V_B = 2 \cdot V$   
 $V_E := V_B - 0.7 \cdot V \quad V_E = 1.3 \cdot V$   
 $I_E := \frac{V_E}{R_E} \quad I_E = 5.91 \cdot mA \quad \simeq \ I_C := I_E$   
 $V_{CE} := V_C - V_E \quad V_{CE} = 5.7 \cdot V > 0.2V$ , OK, is in active region

3)  $R_{B1}$   $I_C := 4 \cdot mA$   $V_C := 6 \cdot V$   $V_E := 2.0 \cdot V$   $R_E$  $I_{RB2} := 0.1 \cdot mA$ 

 $V_{CC} := 12 \cdot V \qquad \text{Given: may NOT neglect } I_B \qquad \beta := 150$   $V_{CC} := 12 \cdot V \qquad V_E := 2.0 \cdot V \qquad V_C := 6 \cdot V \qquad I_{RB2} := 0.1 \cdot \text{mA} \qquad I_C := 4 \cdot \text{mA}$ Find R<sub>E</sub>, R<sub>C</sub>, V<sub>B</sub>, I<sub>B</sub>, R<sub>B2</sub>, and R<sub>B1</sub>:
Solution:  $V_{CE} := 0 \cdot V \qquad V_{CE} := V_{C} - V_{E} \qquad V_{CE} = 4 \cdot V \qquad > 0.2V, \text{ is in active region}$   $I_E \simeq I_C \qquad I_E := I_C \qquad R_E := \frac{V_E}{I_E} \qquad R_E = 500 \cdot \Omega$   $R_C := \frac{V_{CC} - V_C}{I_C} \qquad R_C = 1.5 \cdot k\Omega$ 

 $P_Q := V_{CE} \cdot I_C$   $P_Q = 33.68 \cdot mW$ 

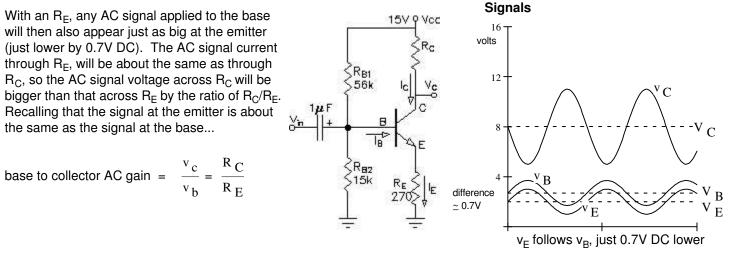
$$R_{C} := \frac{V_{C}}{I_{C}} \qquad R_{C} = 1.5 \cdot k\Omega$$

$$V_{B} := V_{E} + 0.7 \cdot V \qquad V_{B} = 2.7 \cdot V \qquad I_{B} := \frac{I_{C}}{\beta} \qquad I_{B} = 0.027 \cdot mA$$

$$R_{B2} := \frac{V_{B}}{I_{RB2}} \qquad R_{B2} = 27 \cdot k\Omega \qquad R_{B1} := \frac{V_{CC} - V_{B}}{I_{RB2} + I_{B}} \qquad R_{B1} = 73.4 \cdot k\Omega$$

Transistor Notes (BJT) p5

# AC Analysis of Common emitter (CE) amplifier



If a capacitor is placed in parallel with R<sub>E</sub> then the effective AC resistance in the emitter goes way down and the gain goes way up. In that case we need a way to estimate the AC resistance within the base-emitter junction itself.

This is called the small-signal emitter resistance: 
$$r_e = \frac{25 \cdot mV}{I_C}$$

To find the gains when the input has a source resistance and the output is connected to a load resistor, the calculations become a little more complex. YOU DON' T NEED TO KNOW THE FOLLOWING MATERIAL.

R  $_{\rm E}$  is the DC resistance from emitter to ground

R  $_{e}$  is the AC signal resistance from emitter to ground, may be zero

Input impedance:  $R_i = R_{B1} || R_{B2} || \beta (r_e + R_e)$ 

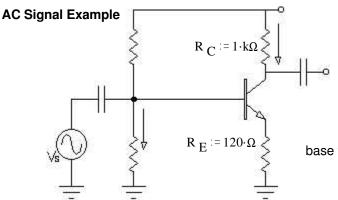
Output impedance:  $R_0 = R_C ||r_0| < --r_0$  Often neglected

AC collector resistance:  $r_c = R_C ||R_L||r_o$ 

r<sub>o</sub> is a characteristic of the transistor, and is often neglected

Voltage gain: 
$$A_v = \frac{v_o}{v_b} = \frac{r_c}{r_e + R_e}$$
  
OR:  $\frac{v_o}{v_s} = \frac{R_i}{R_s + R_i} \cdot \frac{r_c}{r_e + R_e}$   
Current gain:  $A_i = \frac{i_o}{i_i} = \frac{r_c}{r_e + R_e} \cdot \frac{R_i}{R_L} = A_v \cdot \frac{R_i}{R_L}$ 

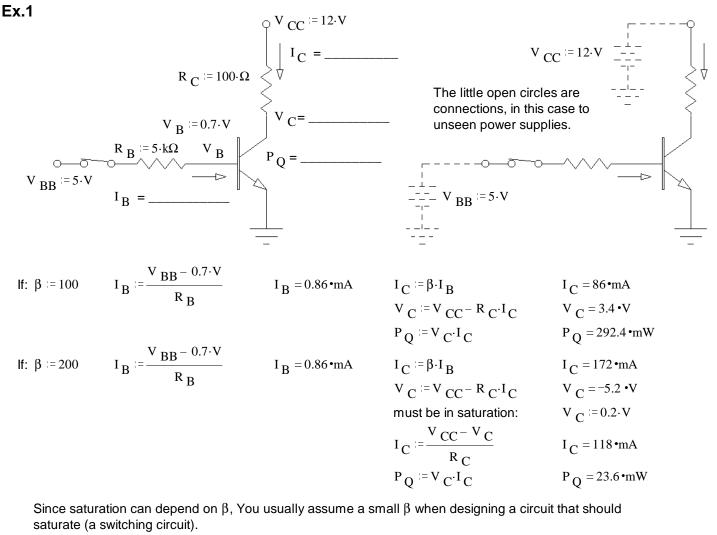
There are several other types of transistor amplifiers, but we won't look at them here.



If the  $v_s$  signal were applied at the base, an AC signal would also appear at the collector. How much larger would it be? (Voltage gain).

base to collector AC gain =  $\frac{v_c}{v_b} = \frac{R_C}{R_E} = 8.33$  times bigger

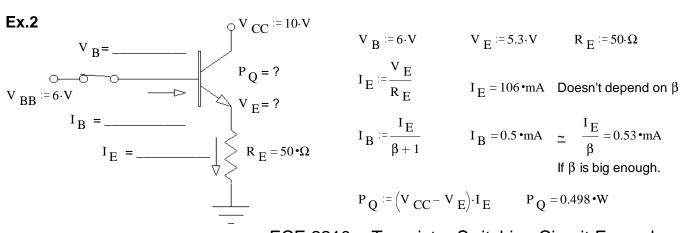
### Transistor Notes (BJT) p6



Saturation also depends on  $R_C$  and  $V_{CC}$ .

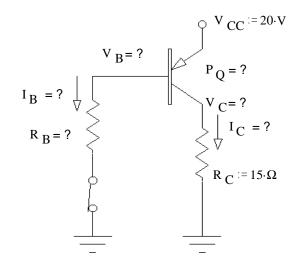
What is the largest value that R<sub>B</sub> could be and still keep the transistor in saturation?

 $I_{Csat} := \frac{V_{CC} - 0.2 \cdot V}{R_{C}}$   $I_{Csat} = 236 \cdot mA$   $I_{B} := \frac{I_{Csat}}{\beta}$   $I_{B} = 1.18 \cdot mA$   $R_{Bmax} = \frac{5 \cdot V - 0.7 \cdot V}{I_{B}} = 3.644 \cdot k\Omega$ 



**Ex.3** If the load must be connected to ground, a PNP transistor is often a better choice.

Let's assume a a small  $\beta$  and saturation and find the  $R_B$  necessary.



a small $\beta$ :	β := 20	
$V_{C} = V_{CC}$	- 0.2·V	$V_{C} = 19.8 \cdot V$
$R_{C} = 15 \cdot \Omega$		
$I_{Csat} := \frac{V_C}{R_C}$		$I_{Csat} = 1.32 \cdot A$
$I_B := \frac{I_{Csat}}{\beta}$		$I_B = 66 \cdot mA$
$V_B := V_{CC}$	- 0.7·V	$V_B = 19.3 \cdot V$
$\mathbf{R}_{\mathbf{B}} := \frac{\mathbf{V}_{\mathbf{B}}}{\mathbf{I}_{\mathbf{B}}}$		$R_B = 292 \cdot \Omega$
$P_Q = 0.2 \cdot V \cdot I$	Csat	$P_Q = 264 \cdot mW$

 $I_{B1c} = 1.08 \cdot mA$ 

 $V_{C1c} = 0.2 \cdot V$ 

 $I_{C1c} = 29.6 \cdot mA$ 

 $V_{B2c} = 0.2 \cdot V$ 

 $I_{C2c} = 0 = I_{RCc}$ 

Sometimes one transistor can't  $I_{R2} = ?$ provide enough amplification.  $R_{C} = 30 \cdot \Omega$ Sometimes you want to "invert" the input (make high off and low on).  $R_2 = 500 \cdot \Omega$  $V_{C1} = V_{B2} = ?$  $\beta_2 = 25$  $V_{B1} = ?$   $V_{B1} = ?$   $V_{B2} = ?$   $\beta_1 := 80$   $\beta_1 := 80$ Switch open Switch closed  $I_{B1c} := \frac{5 \cdot V - 0.7 \cdot V}{R_B}$  $I_{B10} = 0$   $V_{B10} = 0.V$  $V_{B20} = 0.7 \cdot V$ assume  $Q_1$  is in saturation  $I_{B20} := \frac{V_{CC} - 0.7 \cdot V}{R_2}$  $I_{C1c} := \frac{V_{CC} - V_{C1c}}{R_2}$  $I_{B20} = 28.6 \text{ } \text{mA}$  $I_{R20} = I_{B20}$  $I_{R20} = 28.6 \text{ mA}$  $\beta_1 \cdot I_{B1c} = 86 \cdot mA$  I<sub>C1</sub> is controlled by R<sub>2</sub>  $I_{C20} = \beta_2 \cdot I_{B20}$  $I_{C20} = 715 \cdot mA$  $V_{B2c} := V_{C1c}$  $V_{C20} = -6.45 \cdot V$  $V_{C20} = V_{CC} - R_{C'I} C_{C20}$  $I_{B2c} = 0$ Q<sub>2</sub> must be in saturation:  $V_{C20} = 0.2 \cdot V$ When the switch is open, current flows in  $I_{C20} := \frac{V_{CC} - V_{C20}}{R_{C}}$ through the load resistor, R<sub>C</sub>, When it is  $I_{C20} = 493.3 \cdot mA$ closed, no current flows though the load. This is an example of logical "inversion". ECE 2210 Transistor Switching Circuit Examples, p2

Ex.4

### Ex.5 Modified from F07 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

a) Assume the transistor is in saturation (fully on) and that switch has been closed for a long time. What is the load current?

$$I_C = ?$$
  
 $I_{Csat} := \frac{V_{CC} - 0.2 \cdot V}{R_I}$   $I_{Csat} = 600 \cdot mA$ 

b)  $\beta = 80$  find the minimum value of V<sub>s</sub>, so that the transistor will be in saturation.

$$I_{Bmin} := \frac{I_{Csat}}{\beta}$$
  $I_{Bmin} = 7.5 \cdot mA$ 

$$V_{Smin} = I_{Bmin} (R_S + R_1) + 0.7 V$$
  $V_{Smin} = 2.8 V$ 

Use this V<sub>S</sub> for the rest of the problem.

c) Does the diode in this circuit ever conduct a significant current? If yes, when and how much?

 $I_{Dmax} = I_{Csat} = 600 \cdot mA$ When the switch opens. from part a)

d) You got a bad transistor.  $\beta = 60$  Find the new I<sub>C</sub>, and V<sub>CE</sub> and P<sub>O</sub>.

 $I_C = \beta \cdot I_{Bmin}$  $I_{C} = 450 \cdot mA$ <sup>I</sup>C = ? Now operating in active region  $V_{CE} = ?$   $V_{CE} = V_{CC} - R_L I_C$   $V_{CE} = 1.4 \cdot V$  $P_Q := V CE \cdot I C$ P<sub>O</sub> = ?  $P_{O} = 0.63 \cdot W$ 

 $\beta = 60$  Use this for the rest of the problem.

c) Find the minimum value of  $R_L$  so that the transistor will be in saturation.

$$I_{B} := \frac{V_{Smin} - 0.7 \cdot V}{R_{S} + R_{1}}$$
$$I_{B} = 7.5 \cdot mA$$
$$I_{Cmax} := \beta \cdot I_{B}$$
$$I_{Cmax} = 450 \cdot mA$$

$$I_{Cmax} = \beta \cdot I_B$$
  $I_{Cmax}$ 

 $I_{Cent} = 600 \cdot mA$ 

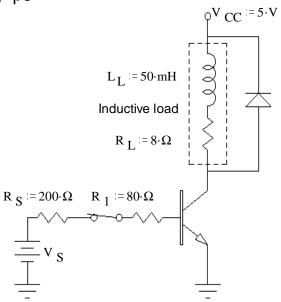
$$R_{Lmin} = \frac{V_{CC} - 0.2 \cdot V}{I_{Cmax}} \qquad R_{Lmin} = 10.7 \cdot \Omega$$

d) 
$$R_1$$
, can't be changed, so find the maximum value of  $R_1$  so that the transistor will be in saturation.

$$I_{Bmin} := \frac{I_{Csat}}{\beta} \qquad I_{Bmin} = 10 \cdot mA$$

$$R_{1max} = \frac{V_{Smin} - 0.7 \cdot V}{I_{Bmin}} - R_{S} = 10 \cdot \Omega$$

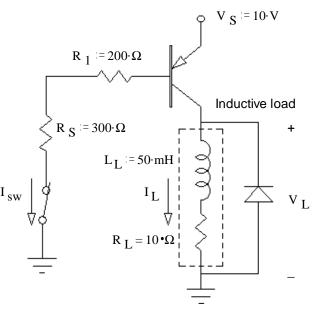
from part a)



### Ex.6 From F05 Final with modifications from F06 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

- a)  $\beta = 25$  Assume the transistor is in the active region, find  $I_{sw}$ ,  $I_L$ ,  $V_L$ ,  $V_{EC}$  and  $P_Q$ .
  - $I_{B} := \frac{V_{S} 0.7 \cdot V}{R_{S} + R_{1}}$   $I_{B} = 18.6 \cdot mA = I_{sw}$   $I_{L} := \beta \cdot I_{B}$   $I_{L} = 465 \cdot mA$   $R_{L} := 10 \cdot \Omega$   $V_{L} := I_{L} \cdot R_{L}$   $V_{L} = 4.65 \cdot V$   $V_{EC} := V_{S} V_{L}$   $V_{EC} = 5.35 \cdot V$   $P_{O} := V_{EC} \cdot I_{L}$   $P_{O} = 2.488 \cdot W$



b) Was the transistor actually operating in the active region? yes no (circle one) yesHow do you know? (Specifically show a value which is or is not within a correct range.)

$$V_{EC} = 5.35 \cdot V > 0.2 \cdot V$$

c) Find the maximum value of  $R_1$ , so that the transistor will be in saturation.

If saturated: 
$$V_{EC} := 0.2 \cdot V$$
  
 $I_{Csat} := \frac{V_S - 0.2 \cdot V}{R_L}$   
 $I_{Csat} = 0.98 \cdot A$   
 $I_{Bmin} := \frac{I_{Csat}}{\beta}$   
 $I_{Bmin} = 39.2 \cdot mA$   
 $R_{1max} = \frac{V_S - 0.7 \cdot V}{I_{Bmin}} - R_S = -63 \cdot \Omega$  NOT POSSIBLE

d) R  $_1 = 200 \cdot \Omega$  and can't be changed, find the minimum value of  $\beta$  so that the transistor will be in saturation.

$$I_{Csat} = 0.98 \cdot A$$
  $\beta_{min} = \frac{I_{Csat}}{I_{B}}$   $\beta_{min} = 52.7$ 

e) How much power is dissipated by the transistor if it has the  $\beta$  you found in part d)

$$P_Q = 0.2 \cdot V \cdot I_{Csat}$$
  $P_Q = 0.196 \cdot W$ 

- f) Does the diode in this circuit ever conduct a significant current? If yes, when and how much? When the switch opens.  $I_{Dmax} = I_{Csat} = 0.98 \cdot A$  from part a)
- g) The switch is open for a while. What is the load current  $(I_f)$  now? 0

### **Ex.7** From F13 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

- a) In order for current to flow in through the load, the switch should be:
   i) closed or ii) open (Circle one)
- b) Assume the switch has been in the position you circled above for a long time. I<sub>L</sub> is 1.3A. Find the power dissipated by transistor  $Q_2$  (neglect base current and  $V_{BE}$ ).

 $I_{L} := 1.3 \cdot A \qquad P_{Q2} = ? \qquad R_{L} := 3 \cdot \Omega$  $V_{CE2} := V_{CC2} - I_{L} \cdot R_{L} \qquad V_{CE2} = 1.1 \cdot V$  $P_{Q2} := V_{CE2} \cdot I_{L} \qquad P_{Q2} = 1.43 \cdot W$ 

c) This is an unacceptable power loss, so you would like to determine the minimum  $\beta_2$  needed so that  $Q_2$  will be in saturation. Assume  $Q_1$  is also in saturation. You may assume  $I_E = I_C$  for both traistors.  $\beta_{2\min} = ?$ 

$$I_{L} := \frac{V_{CC2} - 0.2 \cdot V}{R_{L}} \qquad I_{L} = 1.6 \cdot A = I_{C2}$$

$$V_{E2} := V_{CC2} - 0.2 \cdot V \qquad V_{E2} = 4.8 \cdot V$$

$$V_{B2} := V_{E2} + 0.7 \cdot V \qquad V_{B2} = 5.5 \cdot V$$

$$V_{C1} := V_{B2} + 0.2 \cdot V \qquad V_{C1} = 5.7 \cdot V$$

$$I_{C1} := \frac{V_{CC1} - V_{C1}}{R_{2}} \qquad I_{B2} := I_{C1} \qquad I_{B2} = 57.5 \cdot M \qquad \beta_{2min} = \frac{I_{L}}{I_{B2}} = 27.826$$
Better answer
$$I_{B2} := I_{C1} \cdot \left(\frac{\beta_{1} + 1}{\beta_{1}}\right) \qquad I_{B2} = 58.075 \cdot M \qquad \beta_{2min} = \frac{I_{L}}{I_{B2}} - 1 = 26.551$$

You replace  $Q_2$  with a new transistor that has a  $\beta$  greater than what you just calculated.

d) How much power is dissipated by the new transistor  $Q_2$  (neglect base current and  $V_{BE}$ )?  $P_{O2}$  = ?

$$P_{O2} = 0.2 \cdot V \cdot I_L$$
  $P_{O2} = 320 \cdot mW$ 

e) What is the maximum value of  $R_1$  needed to saturate  $Q_1$ ?  $\beta_1 = 100$ 

$$I_{B1min} := \frac{{}^{1}C1}{\beta_{1}}$$

$$I_{B1min} = 0.575 \cdot mA$$

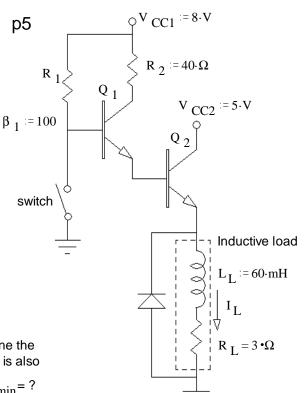
$$V_{B1} := V_{B2} + 0.7 \cdot V$$

$$V_{B1} = 6.2 \cdot V$$

$$R_{1max} := \frac{V_{CC1} - V_{B1}}{I_{B1min}}$$

$$R_{1max} = 3.13 \cdot k\Omega$$

f) Does the diode in this circuit ever conduct a significant current? If yes, when and how much? When the switch closes.  $I_{Dmax} = I_L = 1.6 \cdot A$  from part c)



### **Ex.8** From F12 Final

A couple of transistors are used to control the current flow through an inductive load. The switch has been closed, as shown, for a long time.

a) You measure the voltage at each collector (referenced to ground) as shown on the drawing. Find the power dissipated by transistor  $Q_2$ .

$$V_{C1} := 5 \cdot V \qquad V_{C2} := 2 \cdot V$$

$$I_{L} := \frac{V_{CC} - 2 \cdot V}{R_{L}} \qquad I_{L} = 1.5 \cdot A$$

$$P_{Q2} := V_{C2} \cdot I_L \qquad P_{Q2} = 3 \cdot W$$

b) Find the  $\beta$  of transistor  $Q_2$  .

$$V_{R2} := 5 \cdot V - 0.7 \cdot V$$
  
 $V_{R2} := 4.3 \cdot V$   
 $I_{R2} := \frac{V_{R2}}{R_2}$   
 $\beta_2 := \frac{I_L}{I_{R2}}$   
 $\beta_2 = 34.884$ 

c) Find the  $\beta$  of transistor  $Q_1$ .

$$I_{R1} := \frac{V_{CC} - 0.7 \cdot V}{R_1}$$
  $\beta_1 := \frac{I_{R2}}{I_{R1}}$   $\beta_1 = 58.9$ 

d) Find the minimum  $\beta$  for transistor Q<sub>1</sub> to be in saturation.  $\beta_{1\min} = ?$ 

If  $Q_1$  is saturated:  $V_{R2} = V_{CC} - 0.2 \cdot V - 0.7 \cdot V$   $V_{R2} = 7.1 \cdot V_{R2}$ 

If Q<sub>1</sub> is saturated: 
$$I_{R2} = \frac{V_{R2}}{R_2}$$
  $I_{R2} = 71 \cdot mA$   $\beta_{1\min} = \frac{I_{R2}}{I_{R1}}$   $\beta_{1\min} = 97.3$ 

You replace  $Q_1$  with a different transistor so that now:  $\beta_1 = 200$  Use this from now on. e) Find the new load current ( $I_L$ ) assuming transistor  $Q_2$  is in the active region.

 $Q_1$  is saturated:  $I_{R2} = 71 \cdot mA$   $I_L = I_{R2} \cdot \beta_2$   $I_L = 2.477 \cdot A$ 

f) Check the assumption that  ${\rm Q}_2$  is in the active region and recaculate  ${\rm I}_L$  if necessary.

$$I_{R2} \cdot \beta_{2} \cdot R_{L} = 9.907 \cdot V \qquad V_{CE2} := V_{CC} - I_{R2} \cdot \beta_{2} \cdot R_{L} \qquad V_{CE2} = -1.907 \cdot V \text{ Not possible}$$

$$Q_{2} \text{ is saturated:} \quad I_{L} := \frac{V_{CC} - 0.2 \cdot V}{R_{L}} \qquad I_{L} = 1.95 \cdot A$$

g) Does the diode in this circuit ever conduct a significant current? If yes, when and how much?

When the switch opens. I  $_{Dmax}$  = 1.95 A from part f)

 $R_{1} := 10 \cdot k\Omega$   $R_{2} := 100 \cdot \Omega$   $R_{2} := 100 \cdot \Omega$