

- a) Find the complete expression for $i_{L}(t)$.
- b) At some time the switch is closed again. Is the time constant different now? If yes, find the new time constant.

4. (14 pts) a) Find the s-type transfer function of the circuit shown. Consider the motor current (I_m) as the "output".

 $V_{S} = 9 \cdot V -$

You <u>MUST</u> show work to get credit. Simplify your expression for H(s) so that the denominator is a simple polynomial.



b) How many poles does this transfer function have?

c) How many zeroes does this transfer function have?

If it has 1 or more, express them (probably in terms of R₁, R₂, L and C).

t = 0

 $L := 18 \cdot mH$

 $R_2 := 100 \cdot \Omega$

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5. (21 pts) An capacitor is used to completely correct the power factor of a load.

Find the following:

- a) The power consumed by the load. $P_L = ?$
- b) The power supplied by the source. $P_{S} = ?$
- c) The source current (magnitude and phase). $I_{S} = ?$
- d) The load can be modeled as 2 parts in series. Draw the model and find the values of the parts.
- 6. (22 pts) Assume that diode D_1 does **NOT** conduct.

Assume that diodes D_2 and D_3 **DO conduct.**

- a) Stick with these assumptions even if your answers come out absurd.
 Find the following:
 - V_{D1} = _____
 - I_{D2} = _____
 - I_{D3} = _____
 - $I_{Vs} =$ _____
- b) Based on the numbers above, was the assumption about $\mathrm{D}_1\,\text{correct}?$ Circle one: yes no

How do you know? (Specifically show a value which is or is not within a correct range.)

- c) Based on the numbers above, was the assumption about D₂ correct? yes no How do you know?
- d) Based on the numbers above, was the assumption about D_3 correct? yes no How do you know?
- 7. (17 pts) A voltage waveform (dotted line) is applied to the circuit shown. Accurately draw the output waveform (v_o) you expect to see. Label important times <u>and</u> voltage levels.











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8. (20 pts) The same input signal (at right) is connected to the op-amp circuits below. Sketch the output waveform for each circuit. Clearly label important voltage levels on each output so that I can tell that you know. The op-amp is connected to +10V & -10V power supplies.







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- 9. (28 pts) A couple of transistors are used to control the current flow through an inductive load.
 - a) The switch has been open for a long time. You measure the voltage at the collector of Q_1 to be the value shown (referenced to ground). What is the minimum β_2 needed to insure that transistor Q_2 is in saturation? You may assume that the emitter current of Q_1 is approximately equal to the collector current of Q_1 . $\beta_{2min} = ?$

 $R_{2} := 40 \cdot \Omega$ $R_{1} := 10 \cdot k\Omega$ $V_{C1} := 4 \cdot V$ Q_{1} $V_{C1} := 4 \cdot V$ Q_{2} $V_{C1} := 4 \cdot V$ $V_{C1} := 4$

b) Find the power dissipated in transistor Q_2 with this β . $P_{O2} = ?$

c) Find the β of Q_1 , $\beta_1 = ?$

d) Is this a minimum, maximum, or actual value of β_1 ? (circle one) e) Find the power dissipated in transistor Q_2 if $\beta_2 := 25$ P $_{Q2}$ = ?

f) Find the power dissipated in transistor Q_2 if $\beta_2 = 25$ and the switch is closed. $P_{\Omega 2} = ?$

g) The diode in this circuit conducts a significant current:

- A) never.
- B) when the switch first closes.
- C) whenever the switch is closed.

D) always.

(circle one)

- E) when the switch first opens.
- F) whenever the switch is open.
- h) What is the maximum diode current you expect when the switch is cycled. (Answer 0 if it never conducts.)

Assume the β_2 of part a).

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The grades will be posted on my door in alphabetical order under the alias that you provide here. I will not post grades under your real name. The Internet version will be a pdf file which you can download. Both will show the homework, lab, and exam scores of everyone who answers here.

<u>Answers</u>

