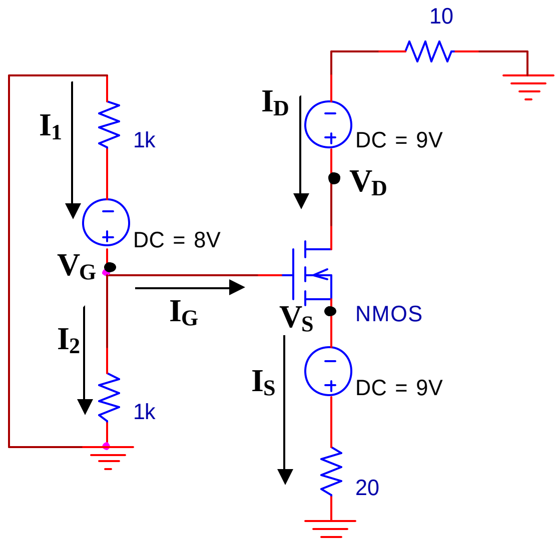
ECE2280 **Homework #5** **Spring 2008**

1. Analyze the circuit shown below to determine the voltages (V_D, V_G, V_S) at all nodes and the currents through all branches (I_1, I_2, I_G, I_D, I_S). Let V_t=2V and $k_n'(W/L)$=180μA/V^2. Neglect the channel length modulation effect (i.e. λ=0).
2. If an AC source is applied at node V_G, state the condition that needs to be satisfied for the ac input amplitude. (what is the small-signal condition?)



Let V_t=2V, $k_n'(W/L)$=180μA/V^2, I_D=I_S=10mA, and λ=0.

2. Draw the small-signal equivalent circuit by assuming all capacitors become shorts.
3. Analyze the circuit to find A_v=V_o/V_{in}, R_{in}(remove 10Ω) and R_{out}
4. Explain how to make the input resistance, R_{in} more ideal. State exact resistors that could be changed and how they could be changed. If this resistors were changed, how does this effect the gain?
5. How could the overall gain be increased. By making that change, what is the tradeoff or what do you have to be careful about?
6. Derive the frequency response transfer function for Vo/Vin in terms of Csig.
7. Find the value of Csig where the low 3db frequency value, f_L = 10 Hz (note this is in Hz – not rad/sec).