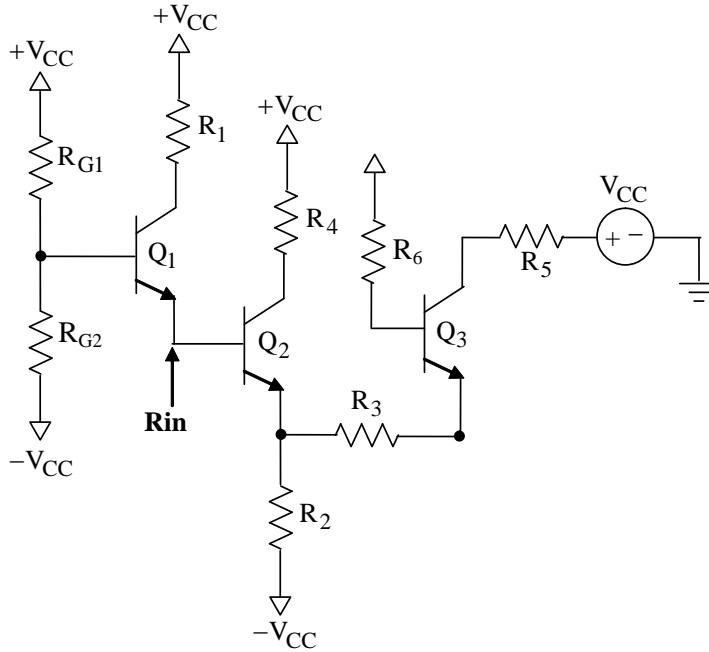
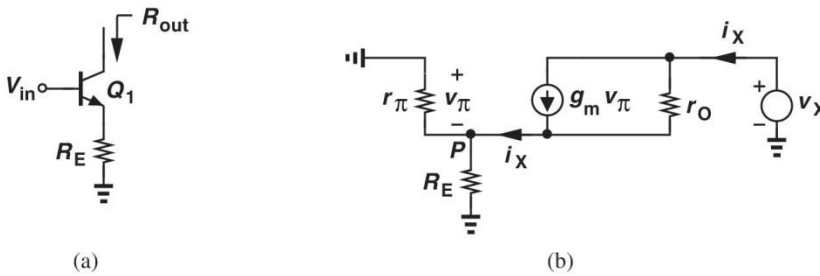


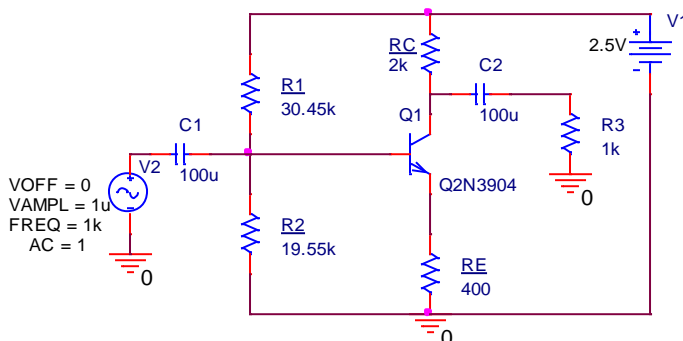
1. (a) Assume the transistors below have a finite β and an infinite Early voltage. Draw the small signal equivalent circuit (hybrid- π or model T)
- (b) Write an expression for the input resistance R_{in} in the circuit shown. Your expression should include *only* real resistances ($R_1, R_2, R_3, R_4, R_5, R_6, R_{G1}, R_{G2}$ or a subset of these) and possibly β and r_{π} . (Assume all transistors have the same β .)



2. Derive R_{out} of the circuit below to prove that $R_{out} = [1 + g_m(R_E || r_{\pi})]r_o + (R_E || r_{\pi})$.



The circuit used below is from homework 4. Modify it to contain the AC source and load resistor. Use this circuit for problems 3-7.



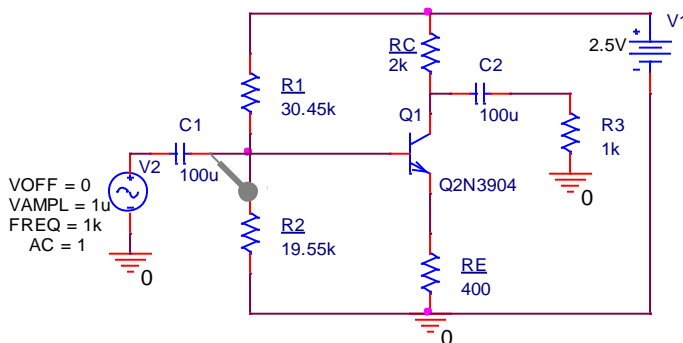
3. (continued)

Redraw the circuit using the hybrid pi model. Assume that $\beta = 125$. Assume all capacitors act as a short for the analysis. Find the following:

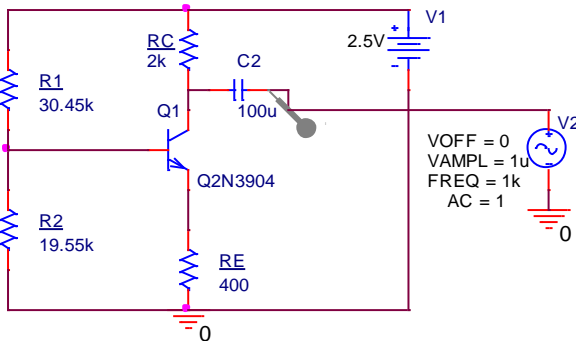
- Overall gain, V_{out}/V_{in} where V_{out} is located above R_3 and V_{in} is to the left of C_1 .
- R_{in}
- R_{out} (ignore the early voltage)

4. Simulate the above circuit in Pspice. Print out the following:

- Circuit schematic
- AC sweep that shows the results for V_{out}/V_{in} . Run the simulation from 0.1 to 1Meg.
- Compare the value to problem 3a.
- Place a current probe as seen in the below circuit. Run an AC sweep and measure the voltage(V_2)/current probe to find R_{in} . The flat region is during the bandwidth. Print this graph. Compare the flat region value(R_{in}) to the value of 3b.



- Remove the input. Remove R_3 (the load) and replace it with a voltage source. Run an AC sweep again and measure the voltage(V_2)/current probe to find R_{out} . Print this graph. Compare the flat region value(R_{out}) to the value of 3c.

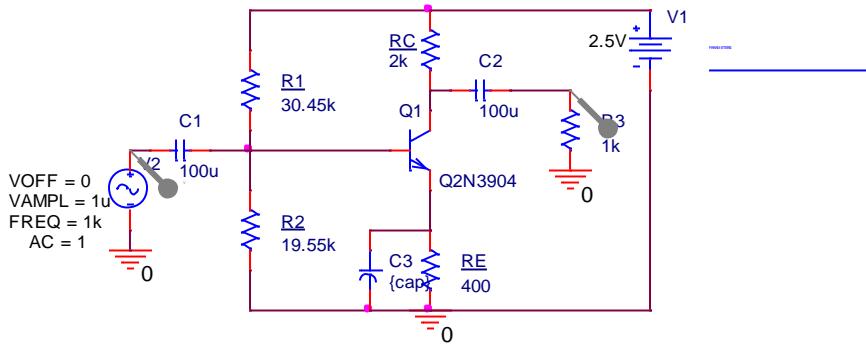


5. Make C_1 into a parameter and perform a parametric AC sweep over C_1 (refer to homework 4 for directions again about parametric sweeps). Vary C_1 using the value list option from: 1n, 10n, 100n, 500n, 1u, 10u, 100u, 1000u.

- Print the results of V_{out}/V_{in} .
- What effect does C_1 have on the circuit?
- If this circuit was used to amplify voice, what value would be acceptable for C_1 (20Hz-40kHz is audio range)?

6. Add a new capacitor as shown below. Change its value into a parameter and perform a parametric AC sweep over C_3 . Perform the AC sweep from 0.1 to 100G. Vary C_2 using the value list options of: 1n, 100n, 500n, 1u, 10u, 100u, 1m.

- a. Print the results of V_{out}/V_{in} .
- b. What effect does $C3$ have on the circuit? Comment on the 1pF value and what is observed.
- c. If this circuit was used to amplify voice, what value would be acceptable.



7. Set the value of $C3$ to 1mF.

- a. Run a transient analysis and print out the circuit showing voltages and currents. Verify that these DC values are the same as from Homework 4.
- b. Run an AC sweep and plot the graph of V_{out}/V_{in} . Print this plot.
- c. State the gain value observed.
- d. Using the method described in problem 4d and 4e, print the graphs that show R_{in} and R_{out} .

8. Draw the hybrid pi model of the circuit in problem 6. Assume that $\beta = 125$. Assume all capacitors act as a short for the analysis. Find the following:

- a. Overall gain, V_{out}/V_{in} where V_{out} is located above $R3$ and V_{in} is to the left of $C1$.
- b. R_{in}
- c. R_{out} (ignore the early voltage)
- d. Compare all these values with those found in problem 7.

9. Modify the core of the circuit again to turn the circuit into a Common Base. Run an AC sweep again and plot the graphs for:

- a. Run a transient analysis on the CB amplifier below and verify that the voltages and currents have remained the same. Print this result.
- b. V_{out}/V_{in} where V_{out} is taken at the collector and V_{in} is at the emitter as shown below. State the value observed for V_{out}/V_{in} .

