#### UNIVERSITY OF UTAH DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING ECE 2280

A. Rasmussen 3/12

# **100 pts Project #2 Simulation:** (Hand in to homework locker by due date) <u>35 pts HAND CALCULATIONS:</u>

- <u>5 pts</u> 1. Draw a Hybrid- $\pi$  equivalent circuit.
- <u>10 pts</u> 2. Calculation of overall Voltage Gain (from hybrid- $\pi$  model).
- <u>10 pts</u> 3. Correct calculation of  $R_{out}$  (from hybrid- $\pi$  model).
- <u>10 pts</u> <u>4.</u> Correct calculation of Ri (from hybrid- $\pi$  model).

### 65 pts SIMULATION:

- <u>5 pts</u> <u>6.</u> Printout of circuit schematic.
- <u>10 pts</u> 7. Verification that both  $I_D$  for ALL transistors are saturated.
  - (Note: Handwrite or circle this on the schematic that shows all the printed voltages/currents. You may have to run a transient simulation to get the values to show.)

# <u>15 pts 8.</u> DC verification of bias point for one stage: Need to show that that VGS> Vt and VDS>VGS-Vt for all transistors in the schematic

- Optionally may Show transfer function (DC sweep of input with output on y-axis).
- <u>15 pts 9.</u> AC Bode plots (shows operation works from 100Hz to 20kHz) of output divided by input (overall gain).
- <u>10 pts</u> 10. Plot of output impedance vs. frequency. (May use directions on class webpage)
- <u>10 pts</u> <u>11</u>. Comparison table between hand and simulation for all  $I_D$ 's (one for each stage), Voltage gain,  $R_{out}$ .

### 100 ptsProject #2 Lab Work: Get this checked by your TA <u>25 pts NOTEBOOK:</u>

- <u>5 pts</u> 1. Check that their lab notebook is organized.
- <u>10 pts</u> 2. Description of the project.
- <u>10 pts</u> 3. Description of the design work.
- <u>5 pts 4.</u> Design Work:
  - 1 pt Schematic of the circuit (PSpice printout or drawn out by hand).
  - 2 pts The voltage gain and  $R_{out}$  hand calculations should correctly match their particular small-signal equivalent circuit and only be different by about 10%.
  - 2 pts Comparison of PSpice simulation versus measured values in particular the midband gain and R<sub>out</sub>.

## 75 pts **PROTOTYPE**:

- <u>10 pts</u> 1. Verification that the gain is at least 80V/V amplification. Phase does not matter.
- <u>10 pts</u> 2.  $1^{st}$  stage has no clipping.
- <u>10 pts</u> 3.  $2^{nd}$  stage has no clipping.
- <u>10 pts 4.</u> Correct amplification at their midband gain.
- 7.5 pts 5. Correct amplification at 200 Hz.
- 7.5 pts 6. Correct amplification at 18 kHz.
- <u>10 pts</u> 7. Verification that the amplifier works with the microphone/speaker circuit.
- <u>10 pts</u> 8. Verify that the circuit does not distort the voice signals.