## ECE 3510 homework \# RL6 Root Locus Design

1. Choice of gain. Each root-locus plot below shows a number of closed-loop pole locations labeled "a", "b", "c", etc.. Each plot has at least two poles. In answering the questions below consider all the closed-loop poles, not just the pole at the labeled location. That is, consider where the other pole(s) are when the gain places the labeled pole at the labeled location.
i) List the closed-loop pole locations (labeled "a", "b", "c", etc.) in order of gain factor, smallest to largest.
ii) List the closed-loop pole locations in order of speed of response (measured as the time to get within 4.4\% of the final step resonse). List them slowest to fastest.
iii) List the closed-loop pole locations which would result in a step response with absolutely no overshoot.
iv) List the closed-loop pole locations (not listed in part b) in order of \% overshoot. List them least to most.
v) List the closed-loop pole locations in order of steady-state error to a step input. List them worst to best.

2. Nise, Ch.8, problem 46. Read p. 193 in Nise book. Modify eq. 4.38 with: $\% \mathrm{OS}=\mathrm{e}^{-\pi \cdot \frac{a}{b}}$ (see Bodson p.34).

Modify eq. 4.42 with: $\quad \mathrm{T}_{\mathrm{s}}=\frac{4}{\zeta \cdot \omega_{\mathrm{n}}}=\frac{4}{\mathrm{a}}$

## Answers

1. a) i) b, e, c, d, a
ii) b, e, c, a, d
OR
$b, e, a, c, d$
iii) b, e, c
iv) d, a
v) all will result in $\mathrm{e}_{\mathrm{ss}}(\infty)=0$ because of open-loop pole at origin. If that were not so then list in order of gain.
b) i) g, j, k, h, i, f
ii) f, g, j, k, h, i
iii) g, j, k, h,
iv) $\mathrm{i}, \mathrm{f}$
v) same as i)
c) i) c, d, e, a, b
ii) c, d, e, b, a
iii) b, c
iv) a, e, d
v) all will result in $\mathrm{e}_{\mathrm{ss}}(\infty)=0$ because of open-loop pole at origin. If that were not so then list in order of gain.
2. a) 102300
b) $11.14 \%$
c) $\mathrm{K}<715000$

ECE 3510 Homework RL6

