15.2

Reference 1

$R_{Th}=0\Omega$

15.2

Reference 2

$R_{Th}=0\Omega$

15.2

3

15.2

4

15.2

NAND Gate 5

$V_{CC}=5V$

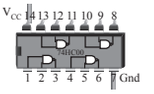
14

$v_{Th1<}$

1 2 3

$R_{Th}\approx 0\Omega$

0 V 7



$v_{Th2<}$

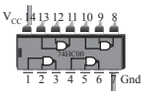
15.2

NAND Gate 6

$v_{Th1<}$

4 5 6

$R_{Th}\approx 0\Omega$



$v_{Th2<}$

15.2

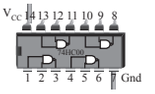
RS Flip-Flop 7

9 10 8

$v_{Th1<}$

$R_{Th}\approx 0\Omega$

12 13 14



$v_{Th2<}$

15.2

XOR Gate 8

$V_{CC}=5V$

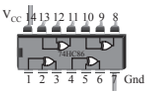
14

$v_{Th1<}$

1 2 3

$R_{Th}\approx 0\Omega$

0 V 7



$v_{Th2<}$