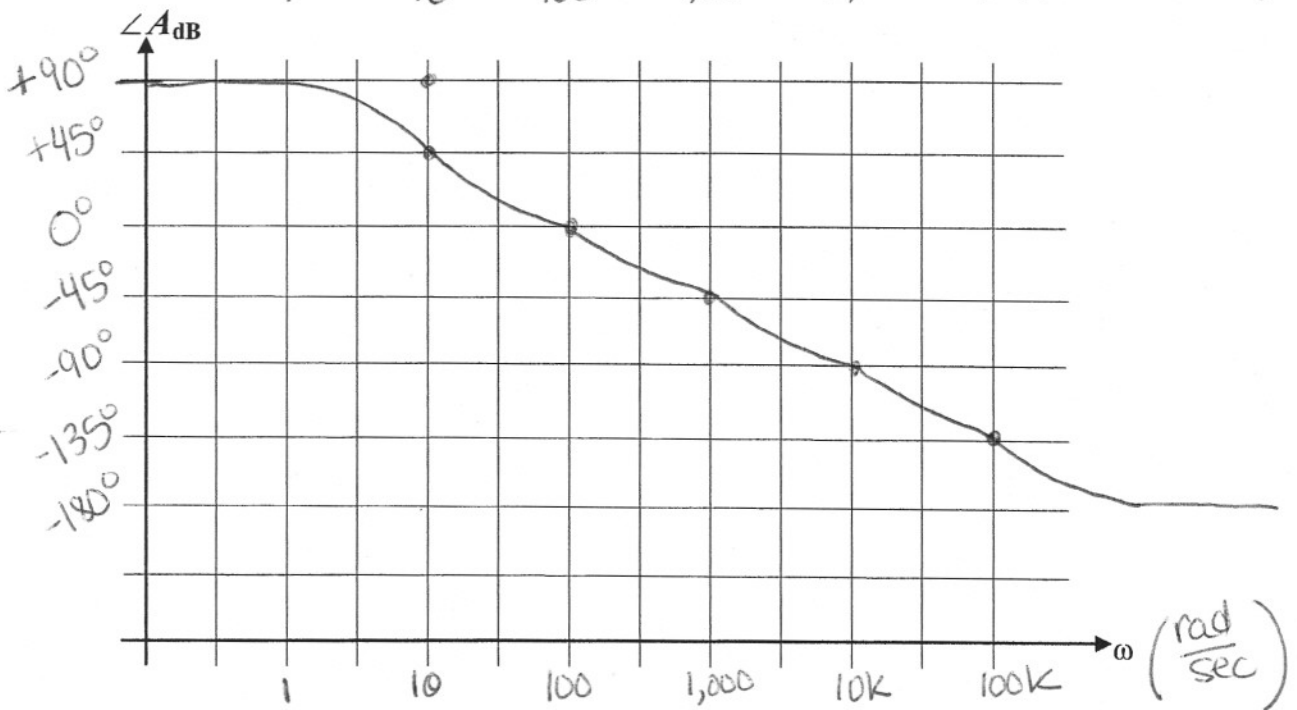
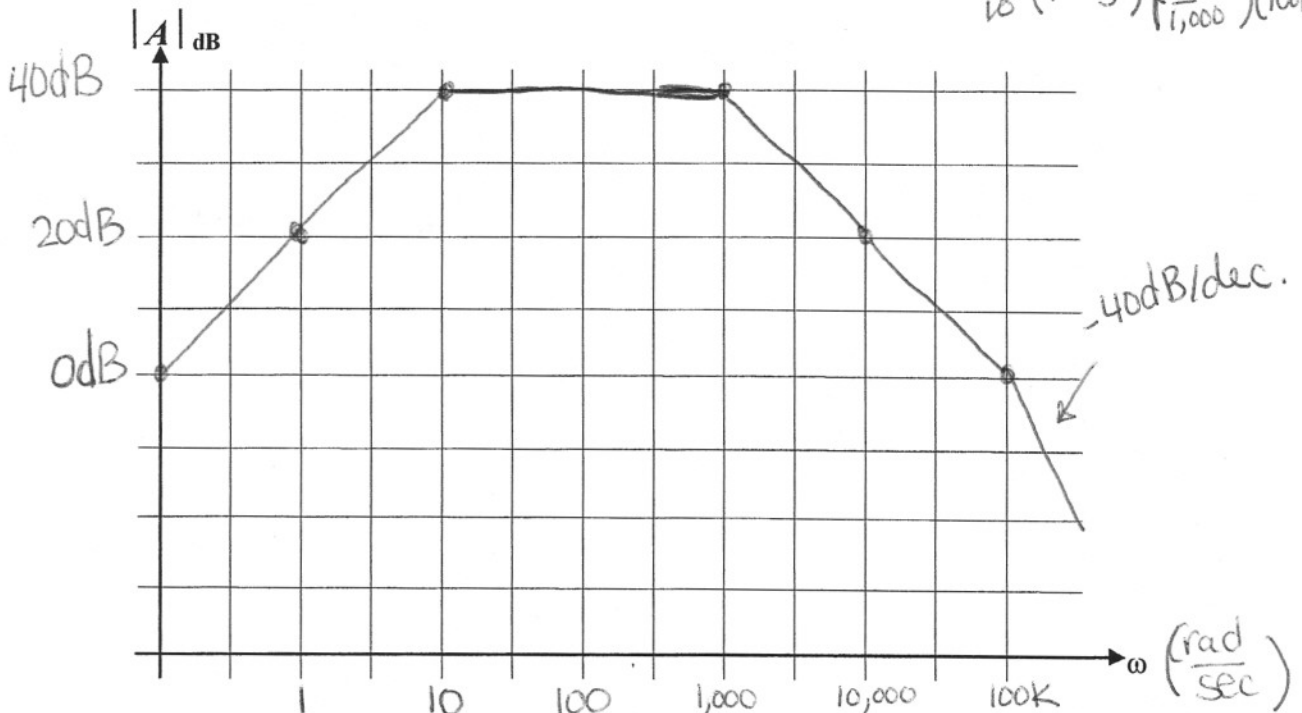


# Exam 2 solution

**Problem 1** – (20 points) An amplifier has the following transfer function:

$$A(s) = \frac{10s}{(s/10+1)(s/1,000+1)(s/100,000+1)} \frac{V}{V} = \frac{10 \cdot s \cdot 10}{\frac{s}{10} \left(1 + \frac{10}{s}\right) \left(\frac{s}{1,000} + 1\right) \left(\frac{s}{100k} + 1\right)}$$



(b) What is  $\beta$  for  $A_f = 20dB$ ? \_\_\_\_\_

(c) What is the lowest closed-loop gain  $A_f$  for which this amplifier has acceptable stability (having a phase margin of at least  $45^\circ$ )?  $A_f = \frac{0.7 V/V}{1}$

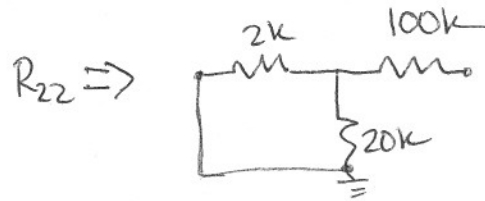
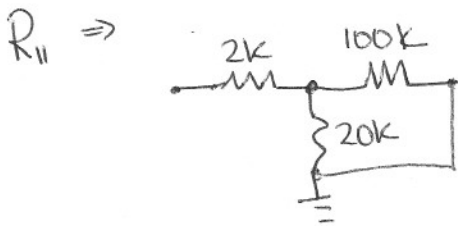
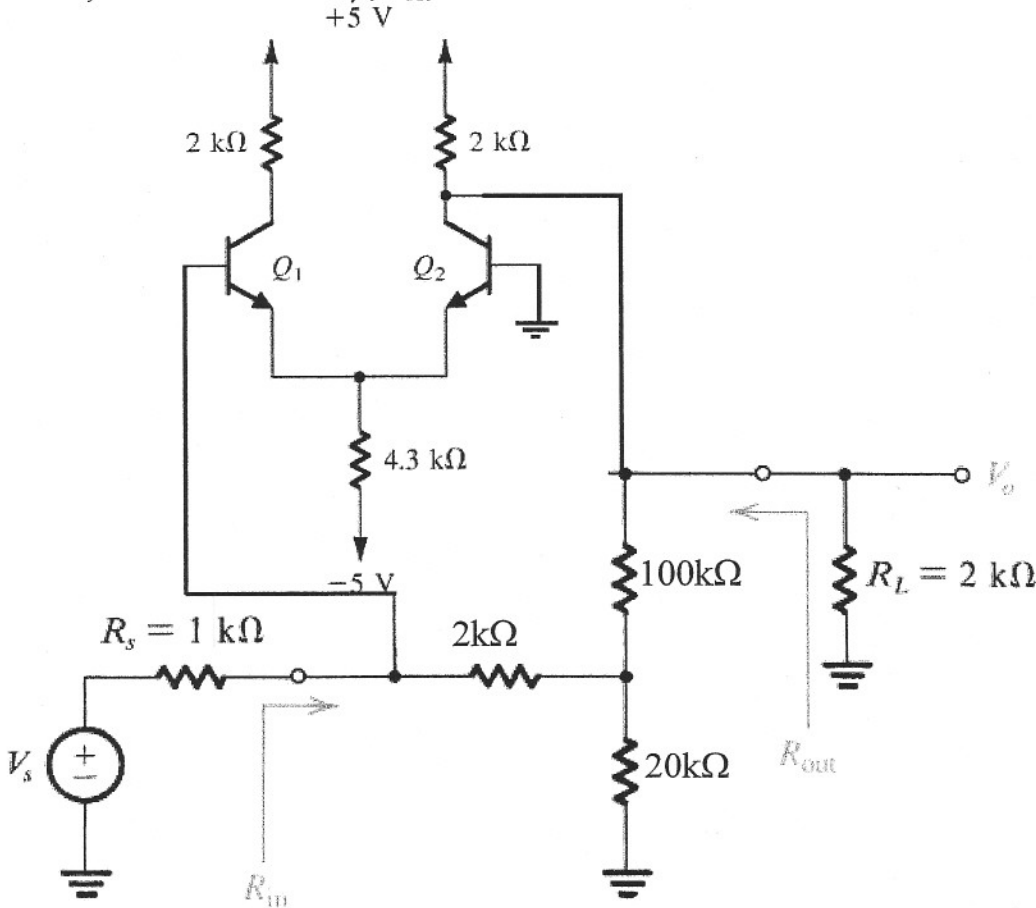
(b)  $A_f = +20dB = 10 V/V = \frac{100}{1+100(\beta)} \Rightarrow \beta(100) = \frac{100}{10} - 1 = 0.09$

(c)  $f = 100k$  for  $PM = 45^\circ \Rightarrow |A \cdot \beta| = 1 = \frac{100 \cdot \beta}{\sqrt{1^2 + (\frac{10}{100k})^2} \sqrt{1^2 + (\frac{100k}{1k})^2} \sqrt{1^2 + 1^2} \cdot \sqrt{2}}$   
 $\therefore \beta = 1.41 \Rightarrow A_f = \frac{100}{1+100(1.41)}$

**Problem 2** - (20 points) What type of feedback topology is shown below (e.g., series-series, shunt-series,...)?

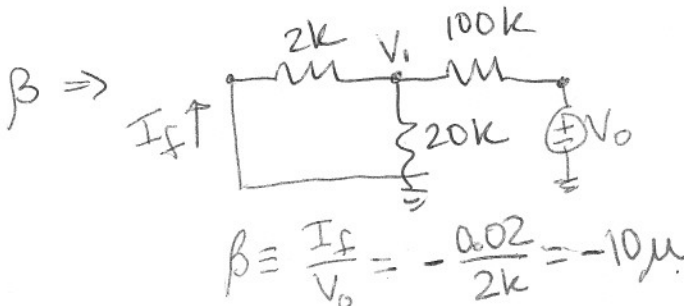
shunt-shunt

Draw three pictures showing the circuit configurations used for measuring  $\beta$ ,  $R_{11}$ , and  $R_{22}$  for the feedback network shown, and find values for  $\beta$ ,  $R_{11}$ , and  $R_{22}$ .



$$R_{11} = 2k + (20k \parallel 100k) \approx 18.7k$$

$$R_{22} = 100k + (2k \parallel 20k) \approx 102k$$



$$\beta \Rightarrow V_1 = \frac{V_o (2k \parallel 20k)}{(2k \parallel 20k) + 100k}$$

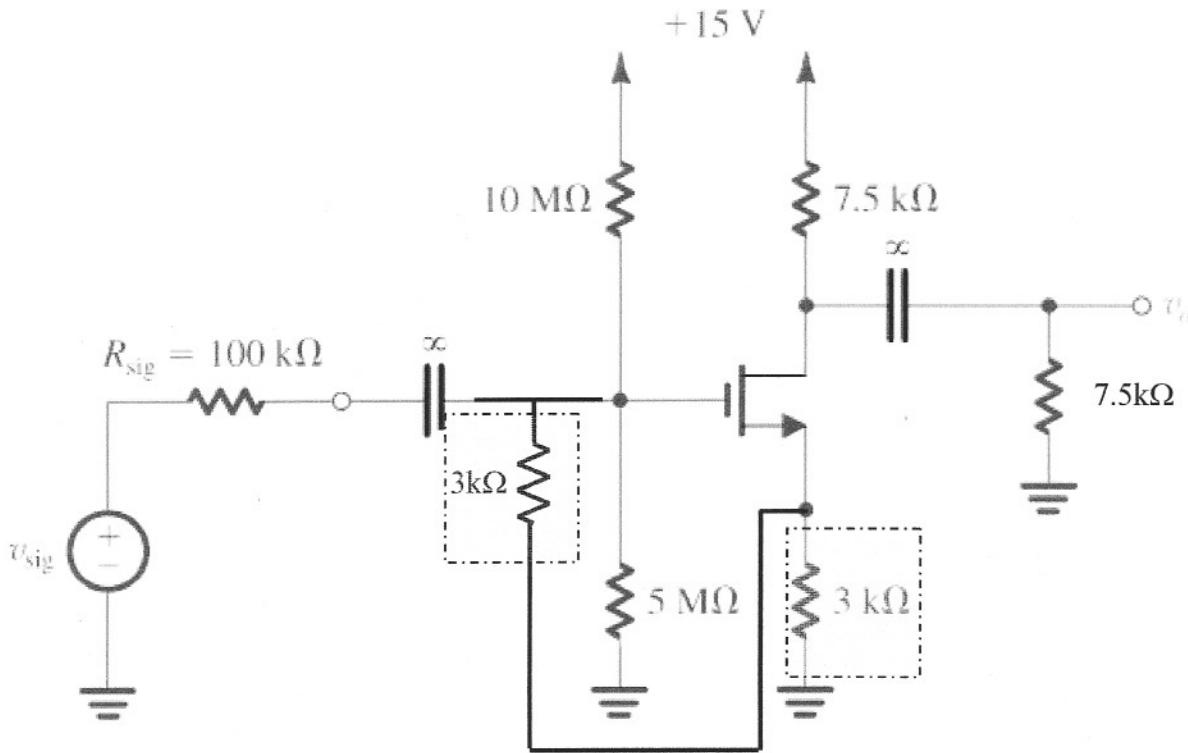
$$V_1 = 0.02 V_o$$

$$V_1 = -I_f (2k)$$

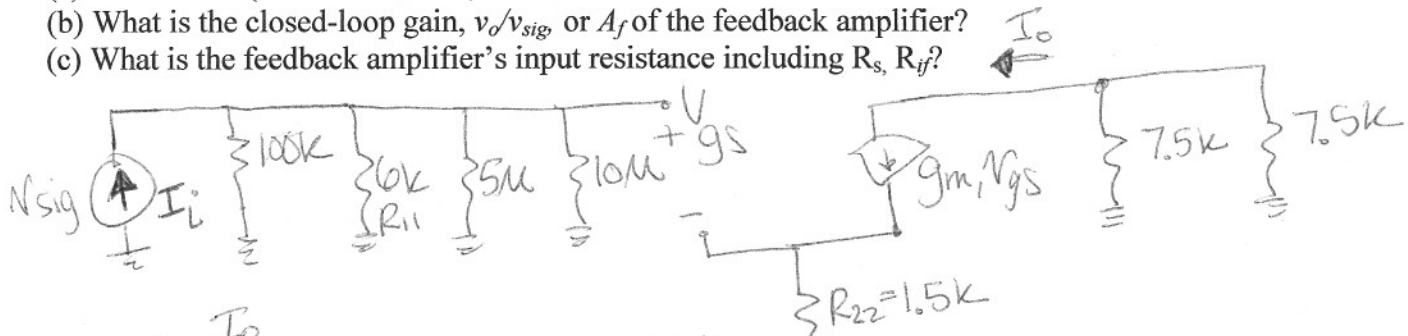
$$0.02 V_o = -I_f (2k)$$

**Problem 3** - (30 points) Assume that the amplifier below is biased at  $I_D=1\text{mA}$ ,  $V_{GS}=2\text{V}$ ,  $V_t=1\text{V}$ . Ignore  $\lambda$ .

The feedback network consists of the two highlighted  $3\text{k}\Omega$  resistors.  $R_{11}=6\text{k}\Omega$ ,  $R_{22}=1.5\text{k}\Omega$ ,  $\beta=-1/2$ .



- (a) What is  $A$ ? (Note that  $v_s \neq 0$ !) shunt-series  
 (b) What is the closed-loop gain,  $v_o/v_{sig}$ , or  $A_f$  of the feedback amplifier?  
 (c) What is the feedback amplifier's input resistance including  $R_s$ ,  $R_{if}$ ?



$$A \equiv \frac{I_o}{I_i}$$

$$g_{m1} = \frac{2I_D}{V_{ov}} = \frac{2(1\text{mA})}{2-1} = 2\text{mA/V}$$

$$I_o = g_{m1} v_{gs}$$

$$v_g = I_i (100\text{k}\Omega \parallel 6\text{k}\Omega \parallel 5\text{M}\Omega \parallel 10\text{M}\Omega) \approx I_i (5650)$$

$$v_s = g_{m1} v_{gs} (1.5\text{k}\Omega)$$

$$v_{gs} = I_i (5650) - g_{m1} (1.5\text{k}\Omega) v_{gs} \Rightarrow v_{gs} = \frac{I_i (5650)}{1 + g_{m1} (1.5\text{k}\Omega)} = 1412.5 I_i$$

$$\frac{I_o}{I_i} = g_{m1} (1412.5) = \boxed{2.825 \text{ A/A}}$$

$$\#3 \text{ b. } A_f = \frac{A}{1+A\beta} = \frac{2.825}{1+(2.825)(-\frac{1}{2})} = \boxed{-6.85 \text{ A/A}}$$

$$A_f = \frac{I_o}{I_{sig}}$$

$$I_{sig} = \frac{V_{sig}}{100k}$$

$$V_o = -I_o \left( \frac{7.5k}{2} \right) \Rightarrow I_o = -267\mu V_o$$

$$A_f = \frac{I_o}{I_{sig}} = \frac{-267\mu V_o \cdot 100k}{V_{sig}} = -6.85$$

$$\therefore \frac{V_o}{V_{sig}} = \boxed{0.3 \text{ V/V}}$$

$$\text{c. } R_i = (100k \parallel 6k \parallel 5M \parallel 10M) = 5650$$

$$R_{if} = \frac{5650}{1+A\beta} = \boxed{13.6k\Omega}$$

**Problem 4 – (15 points)**

The datasheet of the PN2222A bipolar transistor contains the following information:

Maximum allowable junction temperature  $T_{Jmax} = 150^{\circ}\text{C}$

Maximum power dissipation  $P_{Dmax} = 1.5 \text{ W}$  at case temperature  $T_C = 25.5^{\circ}\text{C}$

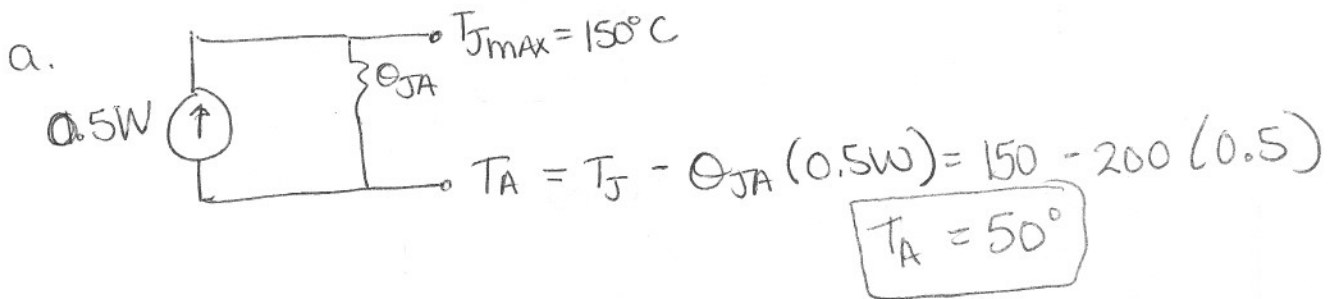
Junction-to-case thermal resistance  $\theta_{JC} = 83^{\circ}\text{C/W}$

Junction-to-ambient thermal resistance  $\theta_{JA} = 200^{\circ}\text{C/W}$

(a) What is the maximum ambient temperature to ensure safe operation at 0.5W? (Note that we are not using a heat sink in this problem.)

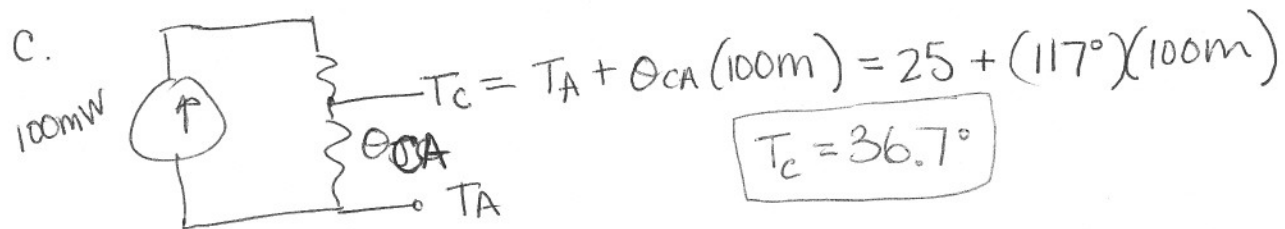
(b) Find the case-to-ambient thermal resistance  $\theta_{CA}$ .

(c) Assuming an ambient temperature of  $25^{\circ}\text{C}$ , what is the case temperature  $T_C$  when the transistor dissipates 100mW?



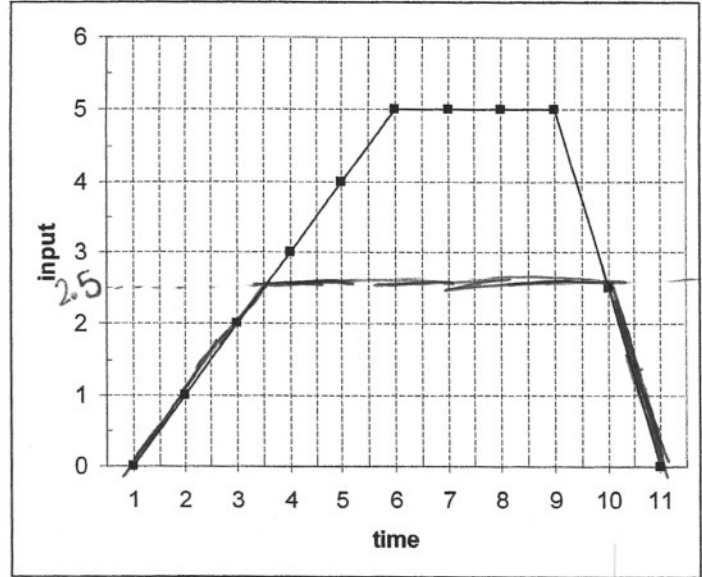
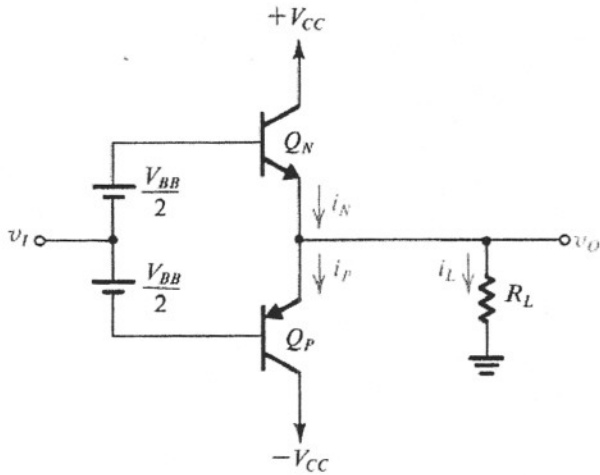
b.

$$(\theta_{CA} + \theta_{JC}) = \theta_{JA} \Rightarrow \theta_{CA} = \theta_{JA} - \theta_{JC} = 200 - 83 = 117^{\circ}\text{C/W}$$

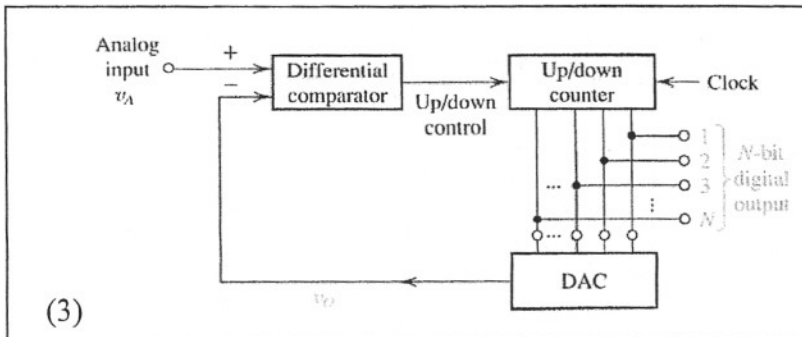
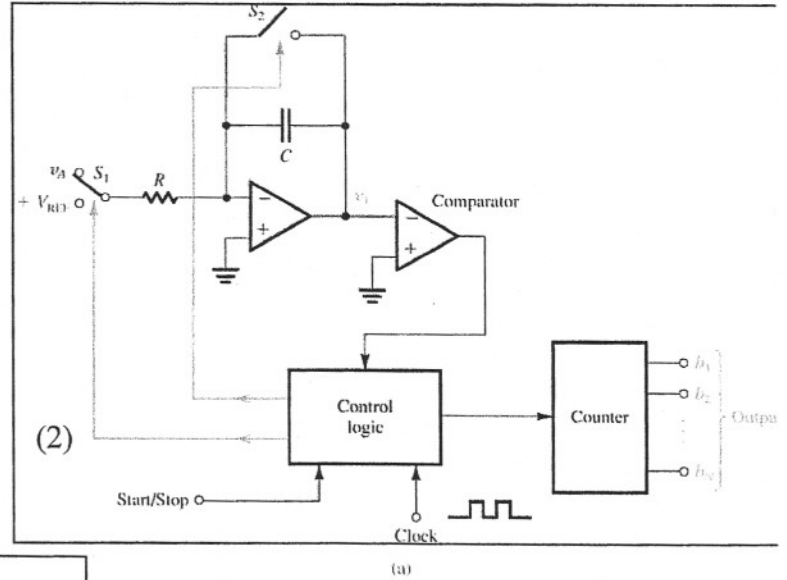
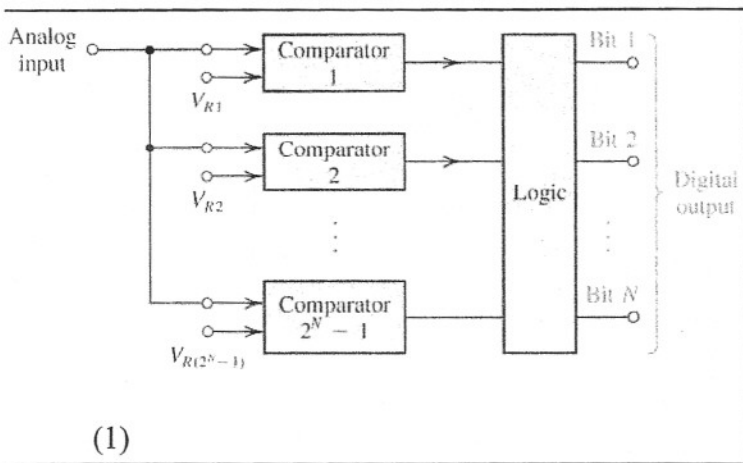


**Problem 5 – (15 points)**

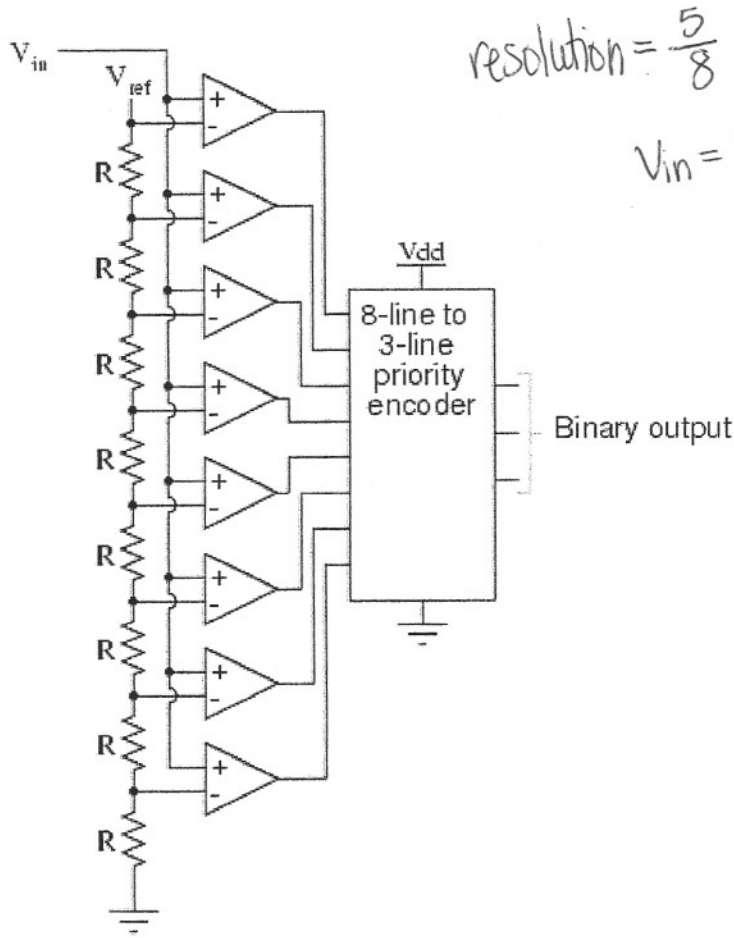
(a) The following output stage has an input,  $v_i$  shown below. Assume the transistors are biased for the correct operation.  $V_{CC} = 3V$ ,  $V_{BE} = 0.5V$  (constant), and  $V_{CEsat} = 0.5V$ . What is the output  $v_o$ ?



- (b) (i) Which analog converter below is the fastest? 1  
 (ii) Which analog converter below is the slowest? 2  
 (iii) Which analog converter below is the most accurate? 2



(c) An analog signal in the range of 0 to +5V is to be digitized. If the circuit shown below is used, what is the resolution of the conversion? If  $V_{in} = 3V$ , what is the binary output?



resolution =  $\frac{5}{8}$

$V_{in} = 3$

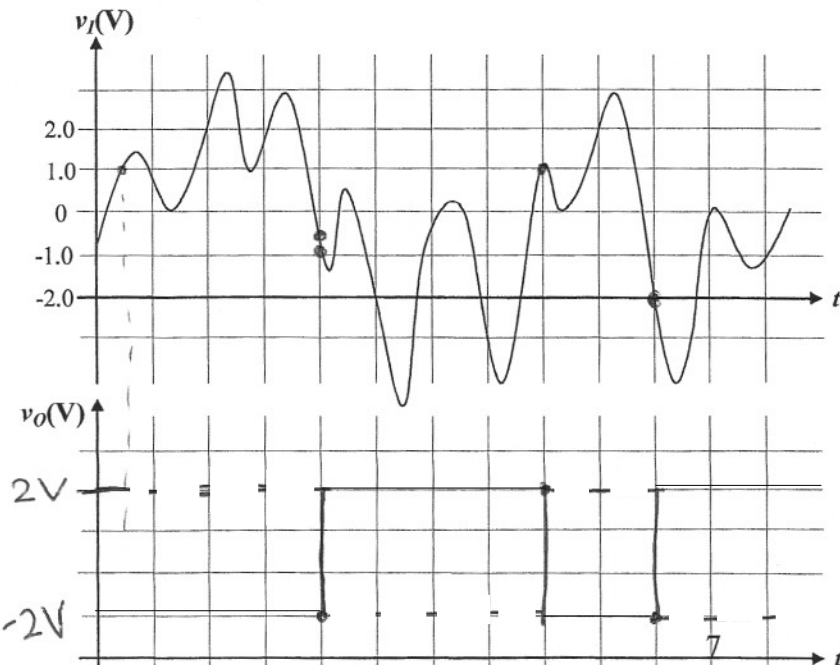
comparators

$11110000 \Rightarrow$

100

$\frac{3}{5/8} = 4.8$

(d) The rail voltages for the op amp shown below are  $\pm 2V$ . Sketch the output seen for the input below. Assume  $R_1 = R_2$ .



$V_+ = \frac{V_o(R_1)}{R_1 + R_2} = V_o\left(\frac{1}{2}\right)$   
 $V_{TH} = 1V$   
 $V_{TL} = -1V$

