

UNIVERSITY OF UTAH
ELECTRICAL AND COMPUTER ENGINEERING DEPARTMENT

ECE 6721: Analog Integrated Circuit Lab

Spring 2010
Thursday 2:00 pm – 5:00 pm
MEB 2265 (Digital Systems Lab)

Instructor: Prof. Reid Harrison
Office: 3114 MEB
Office Hours: Tuesday 10:30 am - 12:00 noon, Thursday 1:00 - 2:00 pm, or by
appointment
Phone: 587-7926
Email: harrison@ece.utah.edu

Corequisite: ECE/CS 5720 or 6720

Text: David A. Johns and Ken Martin, *Analog Integrated Circuit Design*, Wiley, 1997

Web Page: <http://www.ece.utah.edu/~harrison/ece6721/>

TA: Jason Carter
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General Description

This class will give you the opportunity to test and characterize modern CMOS integrated circuits. We will observe aspects of circuit design from single transistor operation to complex amplifiers and data converters. This course will consist of approximately six two-week lab assignments. Each lab assignment will require a written report, which is due in lab one week from the end of each two-week assignment. Lab reports may be turned in up to one week late for a 10% penalty, or two weeks late for a 25% penalty. Reports turned in any later will not be accepted.

The course will occasionally refer to chapters from the textbook, *Analog Integrated Circuit Design*, which is the required textbook for ECE/CS 5720/6720.

You may work in groups of two, and one assignment will be handed in for each group. You are not allowed to share data between groups; every data point shown in your lab report should have been taken by you or your lab partner.

Grading

Grading will be based entirely on lab reports. Lab reports will be graded on clarity and accuracy. A more detailed list of requirements will be handed out for each lab assignment. If everyone in a lab section completes all the work and turns in thorough, complete, accurate lab reports, then everyone will receive a high grade.

Note: The lab will not meet on Jan. 14 (first week of classes), or on Mar. 25 (Spring Break).