EE 1000 Lab 2 Formal Report Grade Breakdown

Communications:
- 5 Organization (ease of locating figures/code/etc.)
- 5 Clarity of style (ease of reading, and etc.)
- 5 English (grammar, punctuation, and etc.)
- 3 Introduction
- 3 Figure titles and numbers
- 3 Equations explained (at least one sentence between equations)
- 3 Matlab listings and comments
- 3 Conclusion, including component model validity, design success, usefulness of device
- 30 Total

Equation Derivations (Part C):
- 5 Circuit simplifications and reasons (why remove diodes, etc.)
- 4 Derive V2p, V2n (step by step, with explanations)
- 5 Derive V1p, V1n (step by step, with explanations)
- 6 Derive Tp, Tn, Tp/Tn (same as above)
- 2 Consistency check(s)
- 20 Total

Circuit Design (Part D):
- 8 Correct Matlab program with explanation to find R1 for Tp/Tn = 2
- 8 Correct Matlab program/derivations to find R4 for Tp = 1s and for Tp = 30 ms
- 5 Derivation of value for R5
- 4 Table of derived/chosen component values
- 25 Total

Circuit Measurements (Part E):
- 2 Actual measured component values for Tp = 1s
- 3 Description of circuit performance with Tp = 1s, Tn = 0.5 s
- 3 Process description to achieve Tp = 30 ms, Tn = 15 ms
- 2 Actual measured values for this case in report and in Matlab
- 3 BASIC description of process to input data into Matlab through Benchlink
- 5 Comparison plot for Tp of Vo, V1 and V2, measured and calculated, with explanations
- 5 Comparison plot for Tn of Vo, V1 and V2, measured and calculated, with explanations
- 2 Actually achieved Tp = 30ms and Tn = 15ms
- 25 Total

100 Grand Total