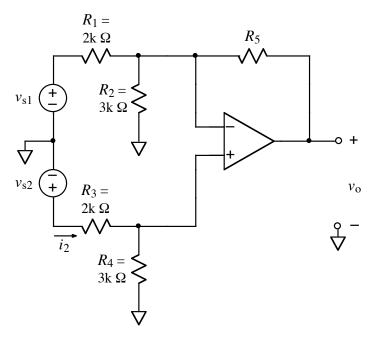


1.



- a) The above circuit operates in linear mode. Derive a symbolic expression for v_0 . The expression must contain not more than the parameters v_{s1} , v_{s2} , R_1 , R_2 , R_3 , R_4 , and R_5 .
- b) If $v_{s1} = 0$ V and $v_{s2} = 1$ V, find the value of R_5 that will yield an output voltage of $v_0 = 1$ V.
- c) Derive a symbolic expression for v_0 in terms of common mode and differential input voltages:

 $v_{\rm cm} \equiv \frac{(v_{s2} + v_{s1})}{2}$ and $v_{\rm dm} \equiv v_{s2} - v_{s1}$

The expression must contain not more than the parameters v_{cm} , v_{dm} , R_1 , R_2 , R_3 , R_4 , and R_5 . Write the expression as v_{cm} times a term plus v_{dm} times a term. Hint: start by writing v_{s1} and v_{s2} in terms of v_{cm} and v_{dm} :

 $v_{s1} = v_{cm} - \frac{v_{dm}}{2}$ and $v_{s2} = v_{cm} + \frac{v_{dm}}{2}$

d) Find the numerical value of the circuit's input resistance, R_{in} , as seen by source v_{s2} . In other words, write a formula for voltage, v_{s2} , divided by i_2 :

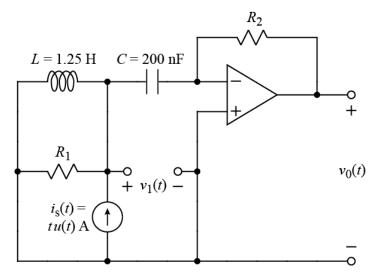
$$R_{\rm in} \equiv \frac{v_{s2}}{i_2}$$

t = 0 $L = 80 \ \mu H$ i(t) $V_{s} = +$ $R_{s} =$ R C = $5 \ \mu F$

After being closed for a long time, the switch opens at t = 0.

The above circuit is an analog "one-shot" circuit that, once charged, produces a short, rounded current-pulse resembling the current that flows in a synapse of a neuron. The circuit is critically damped.

- a) Find the value of *R* that makes the circuit critically-damped.
- b) Using the *R* value from (a), find a numerical expression for the inductor current, i(t), for t > 0.



The current source in the above circuit is off for t < 0.

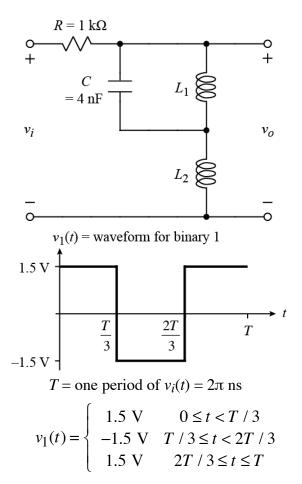
- a) Find a symbolic expression for the Laplace-transformed output, $V_0(s)$, in terms of not more than R_1 , R_2 , L, C, and values of sources or constants.
- b) Choose a numerical value for R_1 to make

$$v_1(t) = v_m - v_m e^{-\alpha t} \left[\cos(\beta t) + \frac{1}{2} \sin(\beta t) \right]$$

where v_m , α , and β are real-valued constants.

Hint: *C* behaves as though it is in parallel with *L* and R_1 .

3.



The above filter circuit is being considered for use in a communication system to detect whether received signals represent binary zeros or binary ones. The plan is to use an inexpensive design with rectangular waveforms (rather than sinusoids). A zero will be signaled by a square wave (not shown), and a one will be signaled by a rectangular wave having 2/3 duty cycle (shown above). The filter for detecting a zero is designed to pass the fundamental frequency of the waveforms, which is the same as one over the period of the waveform shown above. The issues addressed in this problem are the design of the filter and how well it blocks the waveform representing a "one".

- a) Find values of $L_1 \neq 0$ and $L_2 \neq 0$ such that the magnitude of the filter's transfer function, $H(j\omega)$, equals one for the fundamental frequency, ω_0 , and zero for frequency $3\omega_0/2$, (which the engineer proposing the circuit believes is present in the signal for a "one").
- b) Find the numerical value of the magnitude of H for frequency $2\omega_0$.