Student Name		
100 pts Proj 35 pts	ject #3 Simulation: (Hand in to homework locker by due date) HAND CALCULATIONS:	
1. (5 pts)	Draw a Hybrid-π equivalent circuit.	
2. (10pts)	Calculation of overall Voltage Gain (from hybrid- $\pi$ model).	
3. (10pts)	Correct calculation of $R_{out}$ (from hybrid- $\pi$ model).	
4. (10pts)	Correct calculation of Ri (from hybrid- $\pi$ model).	
65 pts	SIMULATION:	
5. (5pts)	Printout of circuit schematic.	
6. (10pts) currents throughout t	Verification that DC values put transistor(s) at an acceptable bias point. (Voltages and the circuit verify transistors are ACTIVE for BJT and SATURATION for MOSFET)	
7. (10pts)	AC Bode plots (10Hz to 200kHz) of overall gain stage with estimated load attached.	
8. (15pts) 100Hz and 30kHz w	Transient simulations that show amplification of at least $80V/V$ for a $1mV$ input at ith estimated load attached.	
9. (10pts)	Plot of output impedance vs. frequency.	
	Comparison table between hand analysis and simulation for DC current and voltage ch node of transistor and current through each branch) for each stage, gain for each stage and $R_{\text{out.}}$	

\_/100\_\_\_\_ **Total** 

<b>Student Name</b>	

FOR REGRADING: Please submit a copy of this graded page along with the work to be regarded. You will receive a 10% reduction from the difference between the regrade and your original grade.

Regrade Comments:

1. (5 pts)	6. (10pts)
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<u>/100</u> Regrade Total