



# Root Locus Design Examples p.3

We have designed a our compensation with the following:

A pole at the origin A zero at -0.1 A zero at -24.28

Gain of 0.418

Find the  $k_p$ ,  $k_i$ , &  $k_d$  of a PID controller.

<sup>k</sup> p

k i

s

 $^{k}d^{\cdot s}$ 

 $(s+0.1)\cdot(s+24.28) = s^2 + 24.38\cdot s + 2.43$ 



30

20

$$gain = k_d = 0.418$$

$$= s^{2} + \frac{k_{p}}{k_{d}} \cdot s + \frac{k_{i}}{k_{d}}$$
$$\frac{k_{i}}{k_{d}} = 2.43 \qquad k_{i} := k_{d} \cdot 2.43 \qquad k_{i} = 1.016$$
$$\frac{k_{p}}{k_{d}} = 24.38 \qquad k_{p} := k_{d} \cdot 24.38 \qquad k_{p} = 10.191 \qquad \text{Notice: } \frac{k_{i}}{k_{p}} = 0.1 \quad \simeq \ 0.1$$

Notice that the proportional gain is actually almost 3 times higher than it was before.  $3 \cdot 3.44 = 10.32$ 



### Root Locus Design Examples p.3

### Problems with the differentiator

1. Tries to differentiate a step input into an impulse -- not likely. You'll have to consider how your differentiator will actually handle a step input and how your amplifier will saturate.

If the differentiator and amplifiers saturate in such a way the the "area under the curve" approximates the impulse "area under the curve", then this may not be such a problem. It may not be as fast as predicted from the linear model, but it may be as fast as the system limits allow. (Pedal-to-the-metal.)

- 2. It's a high-pass filter and can accentuate noise. This is actually common to all compensators that speed up the response.
- 3. Requires active components and a power supply to build. Usually no big deal since your amplifier (source of gain) does too.
- 4. Is never perfect (always has higher-order poles), but then neither is anything else. Especially in mechanical systems, these poles usually are well beyond where they could cause problems.

#### Alternatives:

1. Lag-Lead or PI-Lead compensation. This eliminates the differentiator, but it is still a high-pass filter that can be a noise problem and it could still saturate the amplifier if the input changes too rapidly.

Be sure to check for saturation problems.

2. Place the differentiator in the feedback loop. The output of the plant is much less likely to be a step or to change so rapidly that it causes problems.



Find the  $k_p$ ,  $k_i$ , &  $k_d$  of this controller.

motor tachometer when the output is a position. Then you don't need a separate differentiator circuit, just a separate gain for that signal.

$$\mathbf{F}(s) \cdot \mathbf{C}(s) = \left(k_p + \frac{k_i}{s}\right) \cdot \left(1 + k_d \cdot s\right) = k_p \cdot k_d \cdot \left[\frac{s + \frac{k_i}{k_p}}{s}\right] \cdot \left(\frac{1}{k_d} + s\right) = k_p \cdot k_d \cdot \frac{\left(s + \frac{k_i}{k_p}\right) \cdot \left(s + \frac{1}{k_d}\right)}{s}$$

$$\mathbf{C}(s) = \mathbf{F}(s)$$

For our example:

$$= 0.418 \cdot \frac{(s+0.1) \cdot (s+24.28)}{s}$$

$$k_d := \frac{1}{24.38}$$
  $k_d = 0.041$   
 $k_p := \frac{0.418}{k_d}$   $k_p = 10.191$ 

$$u$$
  
 $k \cdot = k \cdot 0.1$   $k \cdot = 1.019$ 

In this case the open-loop zero in the feedback loop IS NOT in the closed-loop. This turns out to make the step response slower than predicted by the second-order approximation, but try a simulation, you may be able to use significantly more gain with no more overshoot. The differentiator in this position inhibits overshoot.

# PI and PID Design Examples p.5

**Ex.2**, from S16 Exam 3 Consider the transfer function:

$$G(s) := \frac{s+5}{(s+1)\cdot(s^2+4\cdot s+20)}$$



## PI and PID Design Examples p.5

# PI and PID Design Examples p.6

e) What is the gain?

$$k := \frac{1}{|\mathbf{G}_{\mathbf{c}}(s)|} = \left| \frac{(-8+5\cdot \mathbf{j}+1) \cdot [(-8+5\cdot \mathbf{j})^2 + 4\cdot (-8+5\cdot \mathbf{j}) + 20]}{(-8+5\cdot \mathbf{j}+5) \cdot (-8+5\cdot \mathbf{j}+2.5)} \right| = 13.059$$

f) What is the steady-state error for a unit-step input?

$$\mathbf{G}_{\mathbf{c}}(s) := \frac{(s+5)\cdot(s+2.5)}{(s+1)\cdot(s^2+4\cdot s+20)} \qquad \mathbf{G}_{\mathbf{c}}(0) = \frac{(0+5)\cdot(0+2.5)}{(0+1)\cdot(0^2+4\cdot 0+20)} = \frac{(5)\cdot(2.5)}{(1)\cdot(20)} = 0.625$$
$$\mathbf{G}_{\mathbf{c}}(0) = 0.625 \qquad \mathbf{e}_{\text{step}} = \frac{1}{1+k\cdot 0.625} = 10.91\cdot\%$$

g) If this steady-state error was a little too big, what would be the very simplest way to reduce it?

turn up the gain



c) Add a LEAD compensator so that you will be able to place the closed-loop pole at the location found in b).Add the new zero at -30. Find the location of the new pole.

Angles:

from pole at -25

$$\theta_{25} = 180 \cdot \deg - \operatorname{atan} \left( \frac{40}{36 - 25} \right) \qquad \theta_{25} = 105.376 \cdot \deg$$
  
from pole at -40  
 $\theta_{40} = \operatorname{atan} \left( \frac{40}{40 - 36} \right) \qquad \theta_{40} = 84.289 \cdot \deg$   
from pole at -70  
 $\theta_{70} = \operatorname{atan} \left( \frac{40}{70 - 36} \right) \qquad \theta_{70} = 49.635 \cdot \deg$   
from new zero at -30  
 $\theta_{30} = 180 \cdot \deg - \operatorname{atan} \left( \frac{40}{36 - 30} \right) \qquad \theta_{30} = 98.531 \cdot \deg$ 



ii) What is the 2% settling time? Use the second-order approximation.

$$T_s = \frac{4}{36} = 0.111$$
 sec

iii) What is the steady-state error to a unit-step input?

$$\mathbf{G}_{\mathbf{c}}(0) = \frac{100 \cdot (0+30)}{(0+25) \cdot (0+40) \cdot (0+70) \cdot (0+85)} = 5.042 \cdot 10^{-4} \qquad \mathbf{e}_{\text{step}} = \frac{1}{1+\mathbf{k} \cdot \mathbf{G}_{\mathbf{c}}(0)} = 59.161 \cdot \%$$

e) Add another compensator: C  $2(s) = \frac{s+2}{s}$  and maintain the gain of part d)

- i) What is this type of compensator called and what is its purpose?
  - PI, used to eliminate steady-state error
- ii) Calculate what you need to to show that this compensator achieved its purpose.

$$\mathbf{G}_{\mathbf{c}}(\mathbf{s}) \coloneqq \frac{100 \cdot (\mathbf{s} + 30)}{(\mathbf{s} + 25) \cdot (\mathbf{s} + 40) \cdot (\mathbf{s} + 70) \cdot (\mathbf{s} + 85)} \cdot \frac{(\mathbf{s} + 2)}{\mathbf{s}}$$
$$\mathbf{G}_{\mathbf{c}}(0) = \infty \qquad \mathbf{e}_{\text{step}} = \frac{1}{1 + \mathbf{k} \cdot \mathbf{\infty}} = 0 \cdot \%$$

f) With both compensators in place, is there possibility for improvement (quicker settling time speed and/or lower ringing)? If yes, what would be the simplest thing to do? Justify your answer.

A quick sketch of the new root-locus shows that simply decreasing the gain would improve the system



## PI and PID Design Examples p.7

#### ECE 3510 Root Locus Design Crib Sheet

Using 2nd-order approximation: 
$$\frac{N(s)}{(s+a)^2 + b^2} = \frac{N(s)}{s^2 + 2 \cdot a \cdot s + a^2 + b^2} = \frac{N(s)}{s^2 + 2 \cdot \zeta \cdot \omega_n \cdot s + \omega_n^2}$$

$$\omega_n^2 = a^2 + b^2 \qquad \omega_n = \text{natural frequency}$$

$$\zeta \cdot \omega_n = a$$

$$\zeta = \frac{a}{\omega_n} = \frac{a}{\sqrt{a^2 + b^2}} = \text{damping factor} \qquad \zeta = \sin\left(\text{dtan}\left(\frac{a}{b}\right)\right)$$
Overshoot: 
$$OS = e^{-\pi \frac{a}{b}} \qquad \%OS = 100\% \cdot e^{-\pi \frac{a}{b}} \qquad \frac{a}{b} = \frac{\ln(OS)}{-\pi}$$
angle of constant damping line: 
$$90 \cdot \text{deg } + \text{dtan}\left(\frac{a}{b}\right)$$
2% settling time: 
$$T_s = \frac{4}{a} = \frac{4}{\zeta \cdot \omega_n}$$
Time of first peak: 
$$T_p = \frac{\pi}{b}$$
Static error constant (position): 
$$K_p = \lim_{s \to 0} K \cdot C(s) \cdot G(s) \qquad e_{step}(\infty) = e_{step} = \frac{1}{1 + K_p}$$
Nise p378
Lag compensation improves  $K_p$ ,  $K_v$  and  $K_a$  by  $\frac{z}{p_c}$ 

$$\mathsf{IE:} \ K_{pc} \simeq K_{puc} \cdot \frac{z_c}{p_c}$$

Searching along a line of constant damping:

Try s values, choosing b:  $s = -\left(\frac{a}{b} \cdot b\right) + b \cdot j$ Linear interpolation: new  $b = b_1 - \frac{b_2 - b_1}{Im(G(s_2)) - Im(G(s_1))} \cdot Im(G(s_1))$ 

Can also try "a" values with slight modification of the above.

A.Stolp 3/11/09,

Weird forms from Nise book:

 $\% OS = 100\% \cdot e^{\frac{-\zeta \cdot \pi}{\sqrt{1-\zeta^2}}}$  $\sigma_d = a$ p195  $\zeta = \frac{-\ln(OS)}{\sqrt{\pi^2 + (\ln(OS))^2}}$  p195  $T_{p} = \frac{\pi}{\omega_{n} \cdot \sqrt{1 - \zeta^{2}}}$  $\omega_d = b$ p194  $e_{ramp} = \frac{1}{K_v}$ Static error constant (ramp):  $K_{V} = \lim_{s \to 0} s \cdot K \cdot C(s) \cdot G(s)$ p378 (velocity) Static error constant (parabola):  $K_a = \lim_{s \to 0} s^2 \cdot K \cdot C(s) \cdot G(s)$ (acceleration)  $e_{\text{parabola}} = \frac{1}{K_a}$ 

#### ECE 3510 Root Locus Design Crib Sheet p1

## ECE 3510 homework # RL6 Root Locus Design Due: Sat, 3/18

- 1. Choice of gain. Each root-locus plot below shows a number of closed-loop pole locations labeled "a", "b", "c", etc.. Each plot has at least two poles. In answering the questions below consider all the closed-loop poles, not just the pole at the labeled location. That is, consider where the other pole(s) are when the gain places the labeled pole at the labeled location. Use a 2nd order approximation in all cases and neglect the partial-fraction coefficients of the poles
  - i) List the closed-loop pole locations (labeled "a", "b", "c", etc.) in order of gain factor, smallest to largest.
  - ii) List the closed-loop pole locations in order of speed of response (measured as the time to get within 4.4% of the final step resonse). List them slowest to fastest.
  - iii) List the closed-loop pole locations which would result in a step response with absolutely no overshoot.
  - iv) List the closed-loop pole locations (not listed in part b) in order of % overshoot. List them least to most.
  - v) List the closed-loop pole locations in order of steady-state error to a step input. List them worst to best.



а

You may sketch root locus plots and make calculations using a computer program. Questions and problems from Nise are the same for 3rd & 4th editions unless specified otherwise.

Due: Mon. 3/20

- 1. Nise Ch. 9 review questions: 3, 4, 5, 9, 10, 11, & 12.
- Nise Ch. 9 problem 1. For an explanation of the static error constants & calculation of steady-state error, see Nise, section 7.3 or Root Locus Design Crib Sheet. If you use Bodson eq 4.6, include the gain factor (multiply P(0)C(0) by K).

Use G(s) and damping ratio (factor) from 3rd ed:

$$G_{uc}(s) := \frac{1}{(s+3)\cdot(s+6)}$$
 uc =uncompensated  $\zeta := 0.707$ 

Design a PI controller and show that it works.

- 3. Nise Ch. 9 problem 3 Use G(s) and 10% overshoot from 3rd ed:  $G_{uc}(s) := \frac{1}{(s+1)\cdot(s+3)\cdot(s+5)}$ 
  - a) The static error constant is  $\mathbf{K}_{\mathbf{p}}$  on our Crib Sheet.
  - b) Want to improve to  $K_p = 4$  using lag controller.
  - c) I suggest you use the SISO tool to show the improvement.
- 4. Nise Ch. 9 problem 6 Use G(s) from 3rd ed:  $G_{uc}(s) := \frac{1}{(s+1)\cdot(s+2)\cdot(s+3)\cdot(s+6)}$  use:  $\zeta := 0.707$ 
  - a) Shorten settling time to half of what it is without PD compensation.
  - b) Calculate the steady state error for a step input.

For the justification of the 2nd-order assumption, see section 8.7 in Nise. Especially, read the first numbered list and item 3 in the second list. (p. 452 in 3rd ed, p455 in 4th ed, p.416 in 6th ed.)

5. Nise Ch. 9 problem 8 Use G(s) and 20% overshoot from 3rd ed:  $G_{uc}(s) := \frac{1}{s \cdot (s+5) \cdot (s+15)}$ 

a) Shorten settling time to 1/4 of what it is without PD compensation.

b) Change design a lead compensator. Move the zero you found in part a) to -3 and finding the required pole.

6. You have designed a compensator with the following:

A pole at the origin A zero at -0.5 A zero at -10 Gain of 20

Find the  $k_p$ ,  $k_i$ , &  $k_d$  of a PID controller.

<sup>k</sup> p

 $\frac{k_i}{s}$ 

 $^{k}d^{\cdot s}$ 





#### Answers

#### 1. ANSWERS TO REVIEW QUESTIONS

- 1. Chapter 8: Design via gain adjustment. Chapter 9: Design via cascaded or feedback filters.
- 2. A. Permits design for transient responses not on original root locus and unattainable through simple gain adjustments.
- B. Transient response and steady-state error specifications can be met separately and independently without the need for tradeoffs
- 3. PI or lag compensation 4. PD or lead compensation 5. PID or lag-lead compensation
- 6. A pole is placed on or near the origin to increase or nearly increase the system type, and the zero is placed near the pole in order not to change the transient response.
- 7. The zero is placed closer to the imaginary axis than the pole. The total contribution of the pole and zero along with the previous poles and zeros must yield 180° at the design point. Placing the zero closer to the imaginary axis tends to speed up a slow response.
- 8. A PD controller yields a single zero, while a lead network yields a zero and a pole. The zero is closer to the imaginary axis.
- 9. Further out along the same radial line drawn from the origin to the uncompensated poles
- The PI controller places a pole right at the origin, thus increasing the system type and driving the error to zero. A lag network places the pole only close to the origin yielding improvement but not zero error.
- 11. The transient response is approximately the same as the uncompensated system, except after the original settling time has passed. A slow movement toward the new final value is noticed.
- 12. 25 times; the improvement equals the ratio of the zero location to the pole location.
- 13. No; the feedback compensator's zero is not a zero of the closed-loop system.

14. A. Response of inner loops can be separately designed; B. Faster responses possible;

C. Amplification may not be necessary since signal goes from high amplitude to low.

2. Uncompensated: CL pole s  $_{uc}$  := -4.5 + 4.5 · j K  $_{uc}$  := 22.5 44.4% steady-state error

OS = 4.32% T <sub>s</sub> = 0.889 sec

For:  $C(s) = \frac{s+0.1}{s}$  CL pole  $s_c := -4.472 + 4.472 \cdot j$  K  $_{uc} := 22.5$  no steady-state error %OS = 4.32% T  $_s = 0.894$  sec Using 2nd-order approximation

3. Uncompensated:  $s_{\text{uc}} = -1.4 + 1.91 \cdot j$  K = 19.9 steady-state error is about 43%

Compensated, want  $K_p = 4$ , steady-state error of 20% Try:  $C(s) = \frac{s + 0.3}{s + 0.1}$  That should yield a 3x improvement in  $K_p$ .

Matlab output shows a good reduction in steady-state error.

4. Uncompensated:  $s_{uc} := -1.05 + 1.05 \cdot j$  K := 16.65 Want  $s_{c} := -2.1 + 2.1 \cdot j$  Need zero at - 0.604

 $v_{ant} = s_{c}^{--2.1+2.1}$  Need 2ero at -0.004

Possible problems with<br/>the 2nd-order assumption:Pole at -0.771 is not close enough to the zero at -0.604 to cancel it.<br/>Pole at -7.03 is not 5 times farther from j $\omega$  axis than -2.1.

b) 0.753 75% error! That zero close to the origin is NOT OK.

5. Uncompensated:  $s_{uc} := -1.809 + 3.533 \cdot j$  K := 258 Want  $s_{c} := -7.236 + 14.132 \cdot j$  Need zero at - 5.422

Compare to example 9.7 (table 9.8), similar to compensated system except gain. Gain is similar to uncompensated system.

b) 
$$C(s) = \frac{s+3}{s+94.43}$$